

ARM® Cortex®-M23
32-bit Microcontroller

NuMicro® Family
NUC1262 Series BSP
Revision History

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Revision 3.00.003 (Released 2023-09-11)

1. Modified I2C_Master sample code to support 10bit mode by compiler option.
2. Changed the verification conditions for read and write operations in I2C_Master to check each address group.
3. Modified VendorName of Nu_DFU.inf.
4. Added SYS_PowerDown_MinCurrent sample code.
5. Updated SPI_Loopback sample code.
6. Updated Windows driver of USBD_Printer_And_HID_Transfer sample code.
7. Fixed FMC_IAP sample code issue.
8. Modified SYS_UnLockReg() time-out handler.
9. Fixed TX hardware FIFO size to 16 and call UART FIFO size from uart.h in USBD VCOM sample code.
10. Fixed return in main() cause hardfault issue.
11. Added timeout handler for infinite loop.
12. Fixed USBD_MassStorage_CDRom crash on Linux.
13. I2C pins enable schmitt trigger and add I2C hangup/recover flow.
14. Updated auto trim to use HW boundary function.
15. Added Voltage Detector (VDET) definitions in SYS driver for NuCodeGen.
16. Added new api used for select timer capture source in Library/StdDriver/inc/timer.h.
17. Added LVR and BOD de-glitch time selection definitions in Library/StdDriver/inc/sys.h.

Revision 3.00.002 (Released 2021-07-05)

1. Updated PDMA configuration sequence in LLSI_PDMA_Marquee sample code.
2. Fixed build error by ldrom/aprom build order in FMC_IAP sample code.
3. Added LVR and BOD de-glitch time selection definitions in sys.h file.
4. Added new api used for select timer capture source in timer.c file.
5. Added Voltage Detector(VDET) definitions in SYS driver for NuCodeGen tool.
6. Updated auto trim to use HW boundary function in USB sample code.
7. I2C pins enabled schmitt trigger and added I2C hangup/recover flow in I2C sample code.

Revision 3.00.001 (Released 2021-03-18)

1. Initial Release

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