NuMicro® Family

Arm® Cortex®-M0-based Microcontroller

Mini57 Series CMSIS BSP
Revision History

For additional information or questions, please contact: Nuvoton Technology Corporation.

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Revision 3.04.000 (Released 2024-12-23)

1. Upgraded CMSIS from V4.5.0 to V6.1.0
2. Enabled I^2C pin schemitt trigger for I^2C sample code.
3. Added SYS_PowerDown_MinCurrent sample code.
4. Updated Keil project settings to support AC6.
5. Minor changes for sample code.
6. Minor bug fix.

Revision 3.03.000 (Released 2021-04-27)

1. Added Apache-2.0 license declaration into BSP.
2. Added USCI_I2C ISP sample code.
3. Minor changes for sample code.
4. Minor bug fix.

Revision 3.02.001 (Released 2019-11-07)

1. Added ISP sample code.
2. Minor changes for sample code.
3. Minor bug fix.

Revision 3.02.000 (Released 2018-11-14)

1. Removed WDT_Polling and WDT_Wakeup sample code.
2. Added WDT_TimeoutWakeupAndReset sample code.
3. Supported Eclipse GCC development.
4. Minor changes for sample code.
5. Minor bug fix.

Revision 3.01.000 (Released 2017-09-22)

1. Updated baud rate calculation for UUART_Open() and UUART_SetLine_Config() functions.
2. Added RegBase sample code.
3. Minor changes for sample code.
4. Minor bug fix.

Revision 3.00.000 (Released 2017-05-08)

1. Primary release version.

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