ARM® Cortex®-M0

32-bit Microcontroller

NuMicro® Family
NUC126 Series BSP
Revision History

For additional information or questions, please contact: Nuvoton Technology Corporation.

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Revision 3.00.009 (Released 2024-10-21)

1. Update SC library and fix SC sample codes run fail.
2. Add option AC=1 in the Keil project ensures that it's compiler 6.
3. Fix infinite loop.

Revision 3.00.008 (Released 2024-05-14)

1. Remove additional UI2C Master EndFlag settings to prevent premature communication stoppage.
2. Correct UI2C Wakeup Slave and EEPROM not displaying execution results.
3. Add slew rate control for SPI sample codes.
4. Add FMC_SPROM sample code.
5. Remove USBD LPM function.
6. Modify ldrom function table locating.
7. Add process when s_Event is SLAVE_SEND_DATA after receiving SLA+R and returned ACK in USCI_I2C_Slave sample code.
8. Use "volatile" with the function pointer to disable compiler optimizations in I2C/UI2C sample code.
9. Add a reaction in TX/RX when encountering a NACK event after ARBLOIF has been triggered in UI2C_Master sample code.
10. Modify SYS_PowerDown_MinCurrent and RegBased/FMC_IAP sample code.
11. Update USBD HID keyboard sample code to support LED status.

Revision 3.00.007 (Released 2023-09-11)

1. Modified ADC_PWMTrigger sample code print message for IAR Tiny printf formatter.
2. Modified USCI_UART_TxRx Function and FMC_IAP sample code.
3. Revised startup_NUC126.S and retarget.c to fix Hard_Fault can't run code.
4. Modified VendorName of Nu_DFU.inf.
5. Added SYS_PowerDown_MinCurrent sample code.
6. Updated SPI_Loopback sample code.
7. Updated Windows driver of USBD_Printer_And_HID_Transfer sample code.
8. Modified SYS_UnLockReg() time-out handler.
9. Fixed TX hardware FIFO size to 16 and call UART FIFO size from uart.h in USBD VCOM sample code.
10. Removed printf for PowerDown in USBD sample code.

Revision 3.00.006 (Released 2022-08-17)

1. Revised printing error message for I2C sample code.
2. Fixed return in main() cause hardfault issue.
3. Revised infinite loop timeout handler.
4. Fixed USBD_MassStorage_CDROM crash on Linux.
5. Added I2C hang up and recover mechanism for I2C Master and Slave sample code.
6. Opened CESTOPEN in trim function of USBD sample code.
7. Updated auto trim to use SW boundary function of USBD sample code.
8. Enabled schmitt trigger of I2C sample code.
9. Fixed UART TX FIFO control issue in USBD_VCOM sample code.

Revision 3.00.005 (Released 2021-01-21)

1. Fixed USCI_SPI driver issue in Library/StdDriver/src/usci_spi.c
2. Modified to pass USB-IF CV-Chapter 9 & Class test of all USBD Sample code.
3. Fixed USPI_SET_SS_HIGH macro in Library/StdDriver/inc/usci_spi.h

4. Corrected adc definitions for conversion sampling time selection in Library/StdDriver/inc/adc.h
5. Added Apache-2.0 license declaration in driver source.
6. Added README.md file.
7. Added SPI_TRIGGER_TX_RX_PDMA and SPI_DISABLE_TX_RX_PDMA API.

Revision 3.00.004 (Released 2019-10-25)

1. To support GNU GCC.
2. Add emWin supporting.
3. Improve USB driver reliability.
4. Add ISP firmware sample code.

Revision 3.00.003 (Released 2018-01-26)

1. Added CLK_SysTickLongDelay() function for long delay.
2. Improved baud rate calculation accuracy of UUART driver.
3. Improved robust of USB sample code.

Revision 3.00.002 (Released 2017-05-05)

1. Update multi-function pin name definitions.
2. Add single byte and multi byte write/read APIs for I2C.

Revision 3.00.001 (Released 2016-10-17)

1. Initial Release

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