

## NUC200 CMSIS BSP Directory

Directory Introduction for 32-bit NuMicro™ Family

### Directory Information

<b>Document</b>	Driver reference manual and revision history.
<b>Library</b>	Driver header and source files.
<b>SampleCode</b>	Driver sample code.

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## 1 Document Information

Revision History	Show all the revision history about specific BSP.
NuMicro NUC200 Series Driver Reference Guide.chm	Describe the definition, input and output of each API.

## 2 Library Information

<b>CMSIS</b>	CMSIS definitions by ARM® Corp.
<b>Device</b>	CMSIS compliant device header file.
<b>SmartcardLib</b>	Library for accessing a smartcard.
<b>StdDriver</b>	All peripheral driver header and source files.

### 3 Sampel Code Information

<b>CardReader</b>	CCID <sup>[1]</sup> Smart Card reader Sample Code.
<b>Hard_Fault_Sample</b>	Show hard fault information when hard fault happened.
<b>ISP</b>	Sample codes for In-System-Programming.
<b>Template</b>	Software Development Template.
<b>Semihost</b>	Show how to debug with semi-host message print.
<b>RegBased</b>	The sample codes which access control registers directly.
<b>StdDriver</b>	NUC200 Series Driver Samples

1. Circuit card interface device (CCID) is USB device that interface with integrated circuit cards.

## 4 SampleCode\ISP

ISP_DFU	In-System-Programming Sample code through USB interface and following Device Firmware Upgrade Class Specification.
ISP_HID	In-System-Programming Sample code through USB HID interface.
ISP_I2C	In-System-Programming Sample code through I2C interface.
ISP_RS485	In-System-Programming Sample code through RS485 interface.
ISP_SPI	In-System-Programming Sample code through SPI interface.
ISP_UART	In-System-Programming Sample code through UART interface.

## 5 SampleCode\RegBased

### System Manager (SYS)

<b>SYS</b>	Change system clock to different PLL frequency and output system clock from CLK0 pin.
<b>SYS_PowerDown_MinCurrent</b>	Demonstrate how to minimize power consumption when entering power down mode.

### Flash Memory Controller (FMC)

<b>FMC_IAP</b>	Show how to call LDROM functions from APROM. The code in APROM will look up the table at 0x100E00 to get the address of function of LDROM and call the function.
<b>FMC_MultiBoot_SwReset</b>	Show how to use software reset to implement multi-boot system to boot from different applications in APROM.
<b>FMC_RW</b>	Show how to read/program embedded flash by ISP function.

### General Purpose I/O (GPIO)

<b>GPIO_EINTAndDebounce</b>	Show the usage of GPIO external interrupt function and debounce function.
<b>GPIO_INT</b>	Show the usage of GPIO interrupt function.
<b>GPIO_OutputInput</b>	Show how to set GPIO pin mode and use pin data input/output control.
<b>GPIO_PowerDown</b>	Show how to wake up system from Power-down mode by GPIO interrupt.

### PDMA Controller (PDMA)

<b>PDMA</b>	Use PDMA channel 6 to transfer data from memory to memory.
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## Timer Controller (TIMER)

<b>TIMER_Capture</b>	Show how to use the timer2 capture function to capture timer2 counter value.
<b>TIMER_Counter</b>	Implement timer1 event counter function to count the external input event.
<b>TIMER_PeriodicINT</b>	Implement timer counting in periodic mode.
<b>TIMER_PowerDown</b>	Use timer0 toggle-output time-out interrupt event to wake up system.

## Watchdog Timer (WDT)

<b>WDT_PowerDown</b>	Use WDT time-out interrupt event to wake-up system.
<b>WDT_TimeoutINT</b>	Implement periodic WDT time-out interrupt event.
<b>WDT_TimeoutReset</b>	Show how to generate time-out reset system event while WDT time-out reset delay period expired.

## Window Watchdog Timer (WWDT)

<b>WWDT_CompareINT</b>	Show how to reload the WWDT counter value.
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## Real Timer Clock (RTC)

<b>RTC_PowerDown</b>	Use RTC alarm interrupt event to wake-up system.
<b>RTC_TimeAndTick</b>	Get the current RTC data/time per tick.

## PWM Generator and Capture Timer (PWM)

<b>PWM_Capture</b>	Capture the PWMB Channel 1 waveform by PWMB Channel 2.
<b>PWM_DeadZone</b>	Demonstrate how to use PWM Dead Zone function.

<b>PWM_DoubleBuffer</b>	Change duty cycle and period of output waveform by PWM Double Buffer function.
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## UART Interface Controller (UART)

<b>UART_AutoFlow_Master</b>	Demonstrate how to transmit and receive data with auto flow control. The sample code needs to work with <a href="#">UART_AutoFlow_Slave</a> .
<b>UART_AutoFlow_Slave</b>	Demonstrate how to transmit and receive data with auto flow control. The sample code needs to work with <a href="#">UART_AutoFlow_Master</a> .
<b>UART_IrDA_Master</b>	Demonstrate how to transmit and receive data in UART IrDA mode. The sample code needs to work with <a href="#">UART_IrDA_Slave</a> .
<b>UART_IrDA_Slave</b>	Demonstrate how to transmit and receive data in UART IrDA mode. The sample code needs to work with <a href="#">UART_IrDA_Master</a> .
<b>UART_LIN</b>	Demonstrate how to transmit LIN header and response.
<b>UART_PDMA</b>	Transmit and receive UART data with PDMA.
<b>UART_RS485_Master</b>	Demonstrate how to transmit and receive data in UART RS485 mode. The sample code needs to work with <a href="#">UART_RS485_Slave</a> .
<b>UART_RS485_Slave</b>	Demonstrate how to transmit and receive data in UART RS485 mode. The sample code needs to work with <a href="#">UART_RS485_Master</a> .
<b>UART_TxRx_Function</b>	Demonstrate how UART transmit and receive data from PC terminal through RS232 interface.
<b>UART_Wakeup</b>	Show how to wake up system form Power-down mode by UART interrupt.

## Serial Peripheral Interface (SPI)

<b>SPI_Loopback</b>	Implement SPI Master loop back transfer. This sample code needs to connect SPI0_MISO0 pin and SPI0_MOSI0 pin together. It will compare the received data with transmitted data.
<b>SPI_MasterFifoMode</b>	Configure SPI0 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device with FIFO mode. This sample code needs to work with <a href="#">SPI_SlaveFifoMode</a> sample code.
<b>SPI_PDMA_Loopback</b>	Demonstrate SPI data transfer with PDMA. SPI0 will be configured as Master mode and SPI1 will be configured as Slave mode. Both TX PDMA function and RX PDMA function will be enabled.
<b>SPI_SlaveFifoMode</b>	Configure SPI0 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device with FIFO mode. This sample code needs to work with <a href="#">SPI_MasterFifoMode</a> sample code.

## I<sup>2</sup>C Serial Interface Controller (I<sup>2</sup>C)

<b>I2C_EEPROM</b>	Demonstrate how to access EEPROM by I <sup>2</sup> C interface.
<b>I2C_GCMode_Master</b>	Demonstrate how a Master uses I <sup>2</sup> C address 0x0 to write data to I <sup>2</sup> C Slave. Needs to work with <a href="#">I2C_GCMode_SLAVE</a> sample code.
<b>I2C_GCMode_Slave</b>	Demonstrate how to receive Master data in GC (General Call) mode. Needs to work with <a href="#">I2C_GCMode_MASTER</a> sample code.
<b>I2C_Master</b>	Demonstrate how a Master access Slave. Needs to work with <a href="#">I2C_SLAVE</a> sample code.
<b>I2C_Slave</b>	Demonstrate how to set I <sup>2</sup> C in slave mode to receive the data of a Master. Needs to work with <a href="#">I2C_MASTER</a> sample code.
<b>I2C_Wakeup_Master</b>	Demonstrate how to wake-up MCU from power-down. Needs to work with <a href="#">I2C_Wakeup_Slave</a> sample code.

I2C_Wakeup_Slave	Demonstrate how to set I <sup>2</sup> C to wake-up MCU from power-down mode. Needs to work with <a href="#">I2C_Wakeup_Master</a> sample code.
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## I<sup>2</sup>S Controller (I<sup>2</sup>S)

I2S_Master	Demonstrate how I <sup>2</sup> S works in Master mode. This sample code needs to work with <a href="#">I2S_Slave</a> sample code.
I2S_PDMA	Demonstrate how I2S works with PDMA in Master mode. Both TX PDMA function and RX PDMA function will be enabled.
I2S_Slave	Demonstrate how I <sup>2</sup> S works in Slave mode. This sample code needs to work with <a href="#">I2S_Master</a> sample code.

## CRC Controller (CRC)

CRC_8	Implement CRC in CRC-8 mode and get the CRC checksum result.
CRC_CCITT	Implement CRC in CRC-CCITT mode and get the CRC checksum result.

## Analog-to-Digital Converter (ADC)

ADC_ContinuousScanMode	Perform A/D Conversion with ADC continuous scan mode.
ADC_MeasureAVDD	Measure AVDD voltage by ADC.
ADC_PwmTrigger	Demonstrate how to trigger ADC by PWM.
ADC_ResultMonitor	Monitor the conversion result of channel 2 by the digital compare function.
ADC_SingleCycleScanMode	Perform A/D Conversion with ADC single cycle scan mode.
ADC_SingleMode	Perform A/D Conversion with ADC single mode.

## Analog Comparator Controller (ACMP)

ACMP	Demonstrate how ACMP <sup>[1]</sup> works with internal band-gap voltage.
ACMP_Wakeup	Show how to wake up MCU from Power-down mode by ACMP wake-up function.

1. Analog Comparator (ACMP).

## PS/2 Controller (PS/2)

PS2	Demonstrate how to emulate a PS/2 mouse by moving mouse pointer when connecting to PC by PS/2 interface.
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## 6 SampleCode\StdDriver

### System Manager (SYS)

<b>SYS</b>	Change system clock to different PLL frequency and output system clock from CLK0 pin.
<b>SYS_PowerDown_MinCurrent</b>	Demonstrate how to minimize power consumption when entering power down mode.

### Flash Memory Controller (FMC)

<b>FMC_IAP</b>	Show how to execute LDROM functions from APROM. This sample code set VECMAP to LDROM and execute the code in LDROM by functional pointer.
<b>FMC_RW</b>	Show how to read/program embedded flash by ISP function.

### General Purpose I/O (GPIO)

<b>GPIO_EINTAndDebounce</b>	Show the usage of GPIO external interrupt function and de-bounce function.
<b>GPIO_INT</b>	Show the usage of GPIO interrupt function.
<b>GPIO_OutputInput</b>	Show how to set GPIO pin mode and use pin data input/output control.
<b>GPIO_PowerDown</b>	Show how to wake up system from Power-down mode by GPIO interrupt.

### PDMA Controller (PDMA)

<b>PDMA</b>	Use PDMA channel 6 to transfer data from memory to memory.
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## Timer Controller (TIMER)

<b>TIMER_Capture</b>	Show how to use the timer2 capture function to capture timer2 counter value.
<b>TIMER_Counter</b>	Implement timer1 event counter function to count the external input event.
<b>TIMER_PeriodicINT</b>	Implement timer counting in periodic mode.
<b>TIMER_PowerDown</b>	Use timer-0 toggle-output interrupt event to wake-up system.

## Watchdog Timer (WDT)

<b>WDT_PowerDown</b>	Demonstrate how to use WDT time-out interrupt event to wake-up system.
<b>WDT_TimeoutINT</b>	Select one WDT time-out interval period time to generate time-out interrupt event.
<b>WDT_TimeoutReset</b>	Demonstrate how to cause WDT time-out reset system event while WDT time-out reset delay period expired.

## Window Watchdog Timer (WWDT)

<b>WWDT_CompareINT</b>	Select one WWDT window compare value to generate window compare match interrupt event.
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## Real Timer Clock (RTC)

<b>RTC_PowerDown</b>	Use RTC alarm interrupt event to wake-up system.
<b>RTC_TimeAndTick</b>	Get the current RTC data/time per tick.

## PWM Generator and Capture Timer (PWM)

<b>PWM_Capture</b>	Capture the PWMB Channel 1 waveform by PWMB
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	Channel 2.
<b>PWM_DeadZone</b>	Demonstrate how to use PWM Dead Zone function.
<b>PWM_DoubleBuffer</b>	Change duty cycle and period of output waveform by PWM Double Buffer function.

## UART Interface Controller (UART)

<b>UART_AutoFlow_Master</b>	Demonstrate how to transmit and receive data with auto flow control. The sample code needs to work with <a href="#">UART_AutoFlow_Slave</a> .
<b>UART_AutoFlow_Slave</b>	Demonstrate how to transmit and receive data with auto flow control. The sample code needs to work with <a href="#">UART_AutoFlow_Master</a> .
<b>UART_IrDA_Master</b>	Demonstrate how to transmit and receive data in UART IrDA mode. The sample code needs to work with <a href="#">UART_IrDA_Slave</a> .
<b>UART_IrDA_Slave</b>	Demonstrate how to transmit and receive data in UART IrDA mode. The sample code needs to work with <a href="#">UART_IrDA_Master</a> .
<b>UART_LIN</b>	Demonstrate how to transmit LIN header and response.
<b>UART_PDMA</b>	Transmit and receive UART data with PDMA.
<b>UART_RS485_Master</b>	Demonstrate how to transmit and receive data in UART RS485 mode. The sample code needs to work with <a href="#">UART_RS485_Slave</a> .
<b>UART_RS485_Slave</b>	Demonstrate how to transmit and receive data in UART RS485 mode. The sample code needs to work with <a href="#">UART_RS485_Master</a> .
<b>UART_TxRx_Function</b>	Demonstrate how UART transmit and receive data from PC terminal through RS232 interface.
<b>UART_Wakeup</b>	Show how to wake up system form Power-down mode by UART interrupt.



## Smart Card Host Interface (SC)

SC_ReadATR	Read the smartcard ATR from smartcard 0 interface.
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## I<sup>2</sup>S Controller (I<sup>2</sup>S)

I2S_Master	Demonstrate how I <sup>2</sup> S works in Master mode. This sample code needs to work with <a href="#">I2S_Slave</a> sample code.
I2S_PDMA	Demonstrate how I2S works with PDMA in Master mode. Both TX PDMA function and RX PDMA function will be enabled.
I2S_Slave	Demonstrate how I <sup>2</sup> S works in Slave mode. This sample code needs to work with <a href="#">I2S_Master</a> sample code.

## USB Device Controller (USBD)

USBD_Audio_NAU8822	Demonstrate how to implement a USB audio class device. NAU8822 is used in this sample code to play the audio data from Host. It also supports to record data from NAU8822 to Host.
USBD_HID_Keyboard	Show how to implement a USB keyboard device. This sample code supports to use GPIO to simulate key input.

## Serial Peripheral Interface (SPI)

SPI_Loopback	Implement SPI Master loop back transfer. This sample code needs to connect SPI0_MISO0 pin and SPI0_MOSI0 pin together. It will compare the received data with transmitted data.
SPI_MasterFIFOmode	Configure SPI0 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device with FIFO mode. This sample code needs to work with <a href="#">SPI_SlaveFifoMode</a> sample code.

<b>SPI_PDMA_Loopback</b>	Demonstrate SPI data transfer with PDMA. SPI0 will be configured as Master mode and SPI1 will be configured as Slave mode. Both TX PDMA function and RX PDMA function will be enabled.
<b>SPI_SlaveFIFOmode</b>	Configure SPI0 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device with FIFO mode. This sample code needs to work with <a href="#">SPI_MasterFifoMode</a> sample code.

## I<sup>2</sup>C Serial Interface Controller (I<sup>2</sup>C)

<b>I2C_EEPROM</b>	Demonstrate how to access EEPROM by I <sup>2</sup> C interface.
<b>I2C_GCMode_Master</b>	Demonstrate how a Master uses I <sup>2</sup> C address 0x0 to write data to I <sup>2</sup> C Slave. Needs to work with <a href="#">I2C_GCMode_SLAVE</a> sample code.
<b>I2C_GCMode_Slave</b>	Demonstrate how to receive Master data in GC (General Call) mode. Needs to work with <a href="#">I2C_GCMode_MASTER</a> sample code.
<b>I2C_Master</b>	Demonstrate how a Master access Slave. Needs to work with <a href="#">I2C_SLAVE</a> sample code.
<b>I2C_Slave</b>	Demonstrate how to set I <sup>2</sup> C in slave mode to receive the data of a Master. Needs to work with <a href="#">I2C_MASTER</a> sample code.
<b>I2C_Wakeup_Master</b>	Demonstrate how to wake-up MCU from power-down. Needs to work with <a href="#">I2C_Wakeup_Slave</a> sample code.
<b>I2C_Wakeup_Slave</b>	Demonstrate how to set I <sup>2</sup> C to wake-up MCU from power-down mode. Needs to work with <a href="#">I2C_Wakeup_Master</a> sample code.

## CRC Controller (CRC)

<b>CRC_8</b>	Implement CRC in CRC-8 mode and get the CRC checksum result.
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CRC_CCITT	Implement CRC in CRC-CCITT mode and get the CRC checksum result.
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## Analog-to-Digital Converter (ADC)

ADC_ContinuousScanMode	Perform A/D Conversion with ADC continuous scan mode.
ADC_MeasureAVDD	Measure AVDD voltage by ADC.
ADC_PwmTrigger	Demonstrate how to trigger ADC by PWM.
ADC_ResultMonitor	Monitor the conversion result of channel 2 by the digital compare function.
ADC_SingleCycleScanMode	Perform A/D Conversion with ADC single cycle scan mode.
ADC_SingleMode	Perform A/D Conversion with ADC single mode.

## Analog Comparator Controller (ACMP)

ACMP	Demonstrate how ACMP works with internal band-gap voltage.
ACMP_Wakeup	Show how to wake up MCU from Power-down mode by ACMP wake-up function.

## PS/2 Controller (PS/2)

PS2	Demonstrate how to control PS/2 mouse movement on the screen.
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### **Important Notice**

**Nuvoton Products are neither intended nor warranted for usage in systems or equipment, any malfunction or failure of which may cause loss of human life, bodily injury or severe property damage. Such applications are deemed, "Insecure Usage".**

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