

**ARM® Cortex®-M0**  
**32-bit Microcontroller**

**NuMicro® Family**  
**NUC029xGE Series BSP**  
**Revision History**

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**Revision 3.00.004** (Released 2022-08-17)

1. Fixed return in main() cause hardfault issue.
2. Fixed myretarget.c for IAR 8.42.1 and later.
3. Added Blinky sample code for NuMaker board.
4. Added macros for multi-function pin in Library/StdDriver/inc/sys.h.
5. Added timeout handler for infinite loop.
6. Fixed USBD\_MassStorage\_CDROM crash on Linux.
7. Added I2C hang up and recover mechanism for I2C Master and Slave sample code.
8. Modified GPIO number and MFP in Library/Device/Nuvoton/NUC029xGE/Include/NUC029xGE.h.
9. Enabled schmitt trigger of I2C sample code.
10. Fixed UART TX FIFO control issue in USBD\_VCOM sample code.

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**Revision 3.00.003** (Released 2021-01-20)

1. Modified to pass USB-IF CV-Chapter 9 & Class test of all USBD Sample code.
2. Fixed Semihost sample code.
3. Added SPI\_TRIGGER\_TX\_RX\_PDMA and SPI\_DISABLE\_TX\_RX\_PDMA API.
4. Added Apache-2.0 license declaration in driver source.
5. Added README.md file.

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**Revision 3.00.002** (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Added FMCIDLE\_MODULE definition in CLK driver.

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**Revision 3.00.001** (Released 2018-09-21)

1. Initial Release

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