

## M0519 CMSIS BSP Directory

Directory Introduction for 32-bit NuMicro™ Family

### Directory Information

Document	Device driver reference manual and reversion history.
Library	Device driver header and source files.
SampleCode	Device driver sample code.
ThirdParty	Library from the third party.

*The information described in this document is the exclusive intellectual property of Nuvoton Technology Corporation and shall not be reproduced without permission from Nuvoton.*

*Nuvoton is providing this document only for reference purposes of NuMicro microcontroller based system design. Nuvoton assumes no responsibility for errors or omissions.*

*All data and specifications are subject to change without notice.*

For additional information or questions, please contact: Nuvoton Technology Corporation.

[www.nuvoton.com](http://www.nuvoton.com)

# TABLE OF CONTENTS

1. DOCUMENT.....	4
2. LIBRARY .....	5
3. SAMPLE CODE.....	6
4. SAMPLECODE\ISP.....	7
5. SAMPLECODE\REGBASED .....	8
System Manager (SYS).....	8
Flash Memory Controller (FMC).....	8
General Purpose I/O (GPIO).....	8
Timer Controller (TIMER) .....	8
Watchdog Timer (WDT) .....	9
Window Watchdog Timer (WWDT) .....	9
Basic PWM Generator and Capture Timer (BPWM).....	9
Enhance PWM Generator and Capture Timer (EPWM) .....	9
UART Interface Controller (UART).....	9
Serial Peripheral Interface (SPI).....	10
I <sup>2</sup> C Serial Interface Controller (I <sup>2</sup> C).....	11
Enhance 12-bit Analog-to-Digital Converter (EADC) .....	11
Analog Comparator Controller (ACMP) .....	12
OPA (Operational Amplifier).....	12
Hardware Divider (HDIV) .....	12
Enhanced Input Capture Timer (ECAP) .....	12
6. SAMPLECODE\STDDRIVER.....	13
System Manager (SYS).....	13
Flash Memory Controller (FMC).....	13
General Purpose I/O (GPIO).....	13
Timer Controller (TIMER) .....	13
Watchdog Timer (WDT) .....	14
Window Watchdog Timer (WWDT) .....	14
Basic PWM Generator and Capture Timer (BPWM).....	14
Enhance PWM Generator and Capture Timer (EPWM) .....	14

---

UART Interface Controller (UART).....	14
Serial Peripheral Interface (SPI).....	15
I <sup>2</sup> C Serial Interface Controller (I <sup>2</sup> C).....	16
Enhance 12-bit Analog-to-Digital Converter (EADC) .....	16
Analog Comparator Controller (ACMP) .....	17
Hardware Divider (HDIV) .....	17
Enhanced Input Capture Timer (ECAP) .....	17

## 1. Document

Revision History.pdf	Show all the revision history about specific BSP.
NuMicro M0519 BSP Driver Reference Guide.chm	Describe the definition, input and output of each API.

## 2. Library

<b>CMSIS</b>	CMSIS definitions by ARM® Corp.
<b>Component</b>	Library for peripheral components.
<b>Device</b>	CMSIS compliant device header file.
<b>StdDriver</b>	All peripheral driver header and source files.

### 3. Sample Code

<b>FreeRTOS</b>	Simple FreeRTOS™ demo code.
<b>Hard_Fault_Sample</b>	Show hard fault information when hard fault happened.
<b>ISP</b>	Sample codes for In-System-Programming.
<b>Template</b>	Software Development Template.
<b>Semihost</b>	Show how to debug with semi-host message print..
<b>RegBased</b>	The sample codes which access control registers directly.
<b>StdDriver</b>	M0519 Series Driver Samples

#### **4. SampleCode\ISP**

<b>ISP_I2C</b>	In-System-Programming Sample code through I2C interface.
<b>ISP_RS485</b>	In-System-Programming Sample code through RS485 interface.
<b>ISP_SPI</b>	In-System-Programming Sample code through SPI interface.
<b>ISP_UART</b>	In-System-Programming Sample code through UART interface.

## 5. SampleCode\RegBased System Manager (SYS)

<b>SYS</b>	Change system clock to different PLL frequency and output system clock from CLK0 pin.
<b>SYS_PowerDown_MinCurrent</b>	Demonstrate how to minimize power consumption when entering power down mode.

## Fash Memory Controller (FMC)

<b>FMC_IAP</b>	Show how to call LDROM functions from APROM. The code in APROM will look up the table at 0x100E00 to get the address of function of LDROM and call the function.
<b>FMC_MultiBoot</b>	Implement a multi-boot system to boot from different applications in APROM. A LDROM code and 4 APROM code are implemented in this sample code.
<b>FMC_RW</b>	Show how to read/program embedded flash by ISP function.

## General Purpose I/O (GPIO)

<b>GPIO_EINTAndDebounce</b>	Show the usage of GPIO external interrupt function and de-bounce function.
<b>GPIO_INT</b>	Show the usage of GPIO interrupt function.
<b>GPIO_OutputInput</b>	Show how to set GPIO pin mode and use pin data input/output control.
<b>GPIO_PowerDown</b>	Show how to wake up system from Power-down mode by GPIO interrupt.

## Timer Controller (TIMER)

<b>TIMER_Capture</b>	Show how to use the timer2 capture function to capture timer2 counter value.
<b>TIMER_Counter</b>	Implement timer1 event counter function to count the external input event.



TIMER_PeriodicINT	Implement timer counting in periodic mode.
-------------------	--

## Watchdog Timer (WDT)

WDT_PowerDown	Use WDT time-out interrupt event to wake-up system.
WDT_TimeoutINT	Implement periodic WDT time-out interrupt event.
WDT_TimeoutReset	Show how to generate time-out reset system event while WDT time-out reset delay period expired.

## Window Watchdog Timer (WWDT)

WWDT_CompareINT	Show how to reload the WWDT counter value.
-----------------	--

## Basic PWM Generator and Capture Timer (BPWM)

BPWM_Capture	Capture the BPWM0 Channel 0 waveform by BPWM0 Channel 1.
BPWM_DeadZone	Demonstrate how to use BPWM Dead Zone function.
BPWM_DoubleBuffer	Change duty cycle and period of output waveform by BPWM Double Buffer function.

## Enhance PWM Generator and Capture Timer (EPWM)

EPWM_DeadZone	Demonstrate how to use EPWM Dead Zone function.
EPWM_DoubleBuffer	Change duty cycle and period of output waveform by EPWM Double Buffer function.

## UART Interface Controller (UART)

UART_Autoflow_Master	Transmit and receive data with auto flow control. This sample code needs to work with <a href="#">UART_Autoflow_Slave</a> .
UART_Autoflow_Slave	Transmit and receive data with auto flow control. This sample code needs to work with <a href="#">UART_Autoflow_Master</a> .

<b>UART_IrDA_Master</b>	Transmit and receive data in UART IrDA mode. This sample code needs to work with <a href="#">UART_IrDA_Slave</a> .
<b>UART_IrDA_Slave</b>	Transmit and receive data in UART IrDA mode. This sample code needs to work with <a href="#">UART_IrDA_Master</a> .
<b>UART_LIN</b>	Transmit LIN frame including header and response in UART LIN mode.
<b>UART_RS485_Master</b>	Transmit and receive data in UART RS485 mode. This sample code needs to work with <a href="#">UART_RS485_Slave</a> .
<b>UART_RS485_Slave</b>	Transmit and receive data in UART RS485 mode. This sample code needs to work with <a href="#">UART_RS485_Master</a> .
<b>UART_TxRx_Function</b>	Transmit and receive data from PC terminal through RS232 interface.
<b>UART_Wakeup</b>	Show how to wake up system from Power-down mode by UART interrupt.

## Serial Peripheral Interface (SPI)

<b>SPI_Flash_With_FIFO</b>	Demonstrate how to access a Winbond 25Q16 SPI flash with FIFO buffers.
<b>SPI_Flash_Without_FIFO</b>	Demonstrate how to access a Winbond 25Q16 SPI flash without FIFO buffers.
<b>SPI_Loopback</b>	Implement SPI Master loop back transfer. This sample code needs to connect SPI0_MISO pin and SPI0_MOSI pin together. It will compare the received data with transmitted data.
<b>SPI_MasterFifoMode</b>	Configure SPI0 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device with FIFO mode. This sample code needs to work with <a href="#">SPI_SlaveFifoMode</a> sample code.
<b>SPI_SlaveFifoMode</b>	Configure SPI0 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device with FIFO mode. This sample code needs to work with <a href="#">SPI_MasterFifoMode</a> sample code.

## I<sup>2</sup>C Serial Interface Controller (I<sup>2</sup>C)

I2C_EEPROM	Demonstrate how to access EEPROM through a I2C interface.
I2C_GCMode_Master	Demonstrate how a Master uses I2C address 0x0 to write data to I2C Slave. This sample code needs to work with <a href="#">I2C_GCMode_Slave</a> .
I2C_GCMode_Slave	Demonstrate how to receive Master data in GC (General Call) mode. This sample code needs to work with <a href="#">I2C_GCMode_Master</a> .
I2C_Master	Demonstrate how a Master accesses a Slave. This sample code needs to work with <a href="#">I2C_Slave</a> .
I2C_Slave	Demonstrate how to set I2C in slave mode to receive data from a Master. This sample code needs to work with <a href="#">I2C_Master</a> .
I2C_Wakeup_Master	Demonstrate how to wake-up MCU from power-down. Needs to work with <a href="#">I2C_Wakeup_Slave</a> sample code.
I2C_Wakeup_Slave	Demonstrate how to set I <sup>2</sup> C to wake-up MCU from power-down mode. Needs to work with <a href="#">I2C_Wakeup_Master</a> sample code.

## Enhance 12-bit Analog-to-Digital Converter (EADC)

EADC_ADINT_Trigger	Use ADINT interrupt to do the EADC continuous scan conversion.
EADC_PWM_Trigger	Demonstrate how to trigger EADC by BPWM.
EADC_ResultMonitor	Monitor the conversion result of channel 2 by the digital compare function.
EADC_SimultaneousMode	Show how to converts two different input signal at the same time by simultaneous mode of EADC.(Two ADC converters sample simultaneously.)
EADC_SWTRG_Trigger	Trigger EADC by writing ADSSTR register.
EADC_Timer_Trigger	Show how to trigger EADC by timer.

## Analog Comparator Controller (ACMP)

ACMP	Demonstrate how ACMP <sup>[1]</sup> works with internal band-gap voltage.
ACMP_Wakeup	Show how to wake up MCU from Power-down mode by ACMP wake-up function.

[1] Analog Comparator (ACMP).

## OPA (Operational Amplifier)

OPA	Demonstrate how OPA works with schmitt trigger buffer.
-----	--

## Hardware Divider (HDIV)

HDIV	Show how to calculate with hardware divider.
------	--

## Enhanced Input Capture Timer (ECAP)

ECAP	Show how to use ECAP to measure clock frequency
------	---

## 6. SampleCode\StdDriver

### System Manager (SYS)

<b>SYS</b>	Change system clock to different PLL frequency and output system clock from CLK0 pin.
<b>SYS_PowerDown_MinCurrent</b>	Demonstrate how to minimize power consumption when entering power down mode.

### Flash Memory Controller (FMC)

<b>FMC_IAP</b>	Show how to reboot to LDROM functions from APROM. This sample code set VECMAP to LDROM and reset to re-boot to LDROM.
<b>FMC_RW</b>	Show how to read/program embedded flash by ISP function.

### General Purpose I/O (GPIO)

<b>GPIO_EINTAndDebounce</b>	Show the usage of GPIO external interrupt function and de-bounce function.
<b>GPIO_INT</b>	Show the usage of GPIO interrupt function.
<b>GPIO_OutputInput</b>	Show how to set GPIO pin mode and use pin data input/output control.
<b>GPIO_PowerDown</b>	Show how to wake up system from Power-down mode by GPIO interrupt.

### Timer Controller (TIMER)

<b>TIMER_Capture</b>	Show how to use the timer2 capture function to capture timer2 counter value.
<b>TIMER_Counter</b>	Implement timer1 event counter function to count the external input event.
<b>TIMER_Delay</b>	Show how to use timer0 to create various delay time.

TIMER_PeriodicINT	Show how to use timer0 to create various delay time.
-------------------	--

## Watchdog Timer (WDT)

WDT_PowerDown	Use WDT time-out interrupt event to wake-up system.
WDT_TimeoutINT	Implement periodic WDT time-out interrupt event.
WDT_TimeoutReset	Show how to generate time-out reset system event while WDT time-out reset delay period expired.

## Window Watchdog Timer (WWDT)

WWDT_CompareINT	Show how to reload the WWDT counter value.
-----------------	--

## Basic PWM Generator and Capture Timer (BPWM)

BPWM_Capture	Capture the BPWM0 Channel 0 waveform by BPWM0 Channel 1.
BPWM_DeadZone	Demonstrate how to use BPWM Dead Zone function.
BPWM_DoubleBuffer	Change duty cycle and period of output waveform by BPWM Double Buffer function.

## Enhance PWM Generator and Capture Timer (EPWM)

EPWM_DeadZone	Demonstrate how to use EPWM Dead Zone function.
EPWM_DoubleBuffer	Change duty cycle and period of output waveform by EPWM Double Buffer function.

## UART Interface Controller (UART)

UART_AutoFlow_Master	Transmit and receive data with auto flow control. This sample code needs to work with <a href="#">UART_AutoFlow_Slave</a> .
UART_AutoFlow_Slave	Transmit and receive data with auto flow control. This sample code needs to work with <a href="#">UART_AutoFlow_Master</a> .

<b>UART_IrDA_Master</b>	Transmit and receive data in UART IrDA mode. This sample code needs to work with <a href="#">UART_IrDA_Slave</a> .
<b>UART_IrDA_Slave</b>	Transmit and receive data in UART IrDA mode. This sample code needs to work with <a href="#">UART_IrDA_Master</a> .
<b>UART_LIN</b>	Transmit LIN frame including header and response in UART LIN mode.
<b>UART_RS485_Master</b>	Transmit and receive data in UART RS485 mode. This sample code needs to work with <a href="#">UART_RS485_Slave</a> .
<b>UART_RS485_Slave</b>	Transmit and receive data in UART RS485 mode. This sample code needs to work with <a href="#">UART_RS485_Master</a> .
<b>UART_TxRx_Function</b>	Transmit and receive data from PC terminal through RS232 interface.
<b>UART_Wakeup</b>	Show how to wake up system from Power-down mode by UART interrupt.

## Serial Peripheral Interface (SPI)

<b>SPI_Flash_With_FIFO</b>	Demonstrate how to access a Winbond 25Q16 SPI flash with FIFO buffers.
<b>SPI_Flash_Without_FIFO</b>	Demonstrate how to access a Winbond 25Q16 SPI flash without FIFO buffers.
<b>SPI_Loopback</b>	Implement SPI Master loop back transfer. This sample code needs to connect SPI0_MISO pin and SPI0_MOSI pin together. It will compare the received data with transmitted data.
<b>SPI_MasterFIFOmode</b>	Configure SPI0 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device with FIFO mode. This sample code needs to work with <a href="#">SPI_SlaveFifoMode</a> sample code.
<b>SPI_SlaveFIFOmode</b>	Configure SPI0 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device with FIFO mode. This sample code needs to work with <a href="#">SPI_MasterFifoMode</a> sample code.
<b>SPI_SD_Card</b>	Demonstrate how to access a SD card formatted in FAT

file system.

## I<sup>2</sup>C Serial Interface Controller (I<sup>2</sup>C)

I2C_EEPROM	Demonstrate how to access EEPROM through a I2C interface.
I2C_GCMode_Master	Demonstrate how a Master uses I2C address 0x0 to write data to I2C Slave. This sample code needs to work with <a href="#">I2C_GCMode_Slave</a> .
I2C_GCMode_Slave	Demonstrate how to receive Master data in GC (General Call) mode. This sample code needs to work with <a href="#">I2C_GCMode_Master</a> .
I2C_Master	Demonstrate how a Master accesses Slave. This sample code needs to work with <a href="#">I2C_Slave</a> .
I2C_Slave	Demonstrate how to set I2C in slave mode to receive the data from a Master. This sample code needs to work with <a href="#">I2C_Master</a> .
I2C_Wakeup_Master	Demonstrate how to wake-up MCU from power-down. Needs to work with <a href="#">I2C_Wakeup_Slave</a> sample code.
I2C_Wakeup_Slave	Demonstrate how to set I <sup>2</sup> C to wake-up MCU from power-down mode. Needs to work with <a href="#">I2C_Wakeup_Master</a> sample code.

## Enhance 12-bit Analog-to-Digital Converter (EADC)

EADC_ADINT_Trigger	Use ADINT interrupt to do the EADC continuous scan conversion.
EADC_PWM_Trigger	Demonstrate how to trigger EADC by BPWM.
EADC_ResultMonitor	Monitor the conversion result of channel 2 by the digital compare function.
EADC_SimultaneousMode	Show how to converts two different input signal at the same time by simultaneous mode of EADC.(Two ADC converters sample simultaneously.)
EADC_SWTRG_Trigger	Trigger EADC by writing ADSSTR register.



EADC_Timer_Trigger	Show how to trigger EADC by timer.
--------------------	------------------------------------

## **Analog Comparator Controller (ACMP)**

ACMP	Demonstrate how ACMP works with internal band-gap voltage.
------	--

ACMP_Wakeup	Show how to wake up MCU from Power-down mode by ACMP wake-up function.
-------------	--

## **Hardware Divider (HDIV)**

HDIV	Show how to calculate with hardware divider.
------	--

## **Enhanced Input Capture Timer (ECAP)**

ECAP	Show how to use ECAP to measure clock frequency
------	---

### **Important Notice**

**Nuvoton Products are neither intended nor warranted for usage in systems or equipment, any malfunction or failure of which may cause loss of human life, bodily injury or severe property damage. Such applications are deemed, "Insecure Usage".**

**Insecure usage includes, but is not limited to: equipment for surgical implementation, atomic energy control instruments, airplane or spaceship instruments, the control or operation of dynamic, brake or safety systems designed for vehicular use, traffic signal instruments, all types of safety devices, and other applications intended to support or sustain life.**

**All Insecure Usage shall be made at customer's risk, and in the event that third parties lay claims to Nuvoton as a result of customer's Insecure Usage, customer shall indemnify the damages and liabilities thus incurred by Nuvoton.**

---

*Please note that all data and specifications are subject to change without notice.  
All the trademarks of products and companies mentioned in this datasheet belong to their respective owners.*