

**ARM® Cortex®-M**  
**32-bit Microcontroller**

**NuMicro™ Family**  
**M058S Series BSP**  
**Revision History**

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### Revision 3.00.006 (Released 2024-08-05)

1. Used "volatile" with the function pointer to disable compiler optimizations in I2C sample code.
2. Added timeout handler for infinite loop.
3. Enabled LVR in SYS\_PowerDown\_MinCurrent sample code to prevent power on/off fail.
4. Added SYS\_PowerDown\_MinCurrent sample code.
5. Added I2C hang up & recover mechanism for I2C Master and Slave sample code.
6. Updated project settings of ISP\_SPI sample code.

### Revision 3.00.005 (Released 2021-01-18)

1. Fixed warnings of adc and pwm driver.
2. Added Apache-2.0 license declaration in driver source.
3. Added README.md file.
4. Fixed build error of FMC\_IAP IAR sample code.

### Revision 3.00.004 (Released 2019-11-07)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Fixed PWM\_DisableCaptureInt bug of PWM Driver.
4. Fixed CLK driver CLK\_SetHCLK() bug of CLK Driver.
5. Fixed CLK\_EnablePLL() wrong PLL default setting value in CLK Driver

### Revision 3.00.003 (Released 2017-10-20)

1. Fixed clear Receive Line Status interrupt flag bug in UART\_ClearIntFlag().
2. Fixed PLL clock source selection bug in CLK\_SetCoreClock().
3. Fixed UART\_SelectLINMode() clear enable bit setting bug.
4. Add CLK\_SysTickLongDelay() for longer delay time than CLK\_SysTickDelay().
5. Modified to ignore debug message when enabling semihost without NuLink connected.
6. Add constant define "CLK\_CLKSEL0\_STCLK\_S\_HCLK" in CLKSEL0 constant definitions for CLK\_EnableSysTick() function to select HCLK as sysTick clock source.
7. Add new function to control systick and select systick clock source, CLK\_EnableSysTick() and CLK\_DisableSysTick().
8. Add ADC\_MeasureAVDD sample code.

### Revision 3.00.002 (Released 2015-05-07)

1. Fixed UART, I2C, SYS definition error in header file.
2. Fixed reset entry pointer setting of IAR projects.
3. Add VECMAP relative API to FMC driver.
4. Add MCUIRQ control register to GCR\_INT\_T.
5. Add CAPIC control register to PWM\_T.
6. Add ISPSTA control register to FMC\_T.

### Revision 3.00.001 (Released 2015-04-08)

1. First Release.

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