

**ARM® Cortex®-M0**  
**32-bit Microcontroller**

**NuMicro® Family**  
**M0518 Series BSP**  
**Revision History**

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### Revision 3.00.006 (Released 2024-06-19)

1. Used "volatile" with the function pointer to disable compiler optimizations in I2C sample code.
2. Enabled LVR in SYS\_PowerDown\_MinCurrent sample code to prevent power on/off fail.
3. Split FMC\_IAP into FMC\_IAP and FMC\_IAP\_LD.
4. Added SYS\_PowerDown\_MinCurrent sample code.
5. Modified SYS\_UnLockReg() time-out handler.
6. Added timeout handler for infinite loop.
7. Added I2C hang up & recover mechanism for I2C Master and Slave sample code.

### Revision 3.00.005 (Released 2020-09-24)

1. Append \r char before sending \n when use printf.
2. Fixed warnings of ADC driver.
3. Added Apache-2.0 license declaration in driver source.
4. Added README.md file.
5. Fix build error of FMC\_IAP IAR sample code.
6. Fix build warning of GCC Project.

### Revision 3.00.004 (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Fixed CLK\_SetHCLK() bug of CLK driver.
4. Fixed CLK\_EnablePLL() wrong PLL default setting value of CLK driver

### Revision 3.00.003 (Released 2017-10-05)

1. Fix clear Time-out flag method bug in I2C\_ClearTimeoutFlag() of I2C driver.
2. Fix bug of PWM\_ConfigOutputChannel() and PWM\_ConfigCaptureChannel() for counter type setting error when u32ChannelNum is 1,3,5.
3. Fix bug for revised PWM\_Stop() implementation
4. Fix bug for return value error of PWM\_ConfigOutputChannel()
5. Add ADC\_MeasureAVDD sample code
6. Add new function to control systick and select systick clock source, CLK\_EnableSysTick() and CLK\_DisableSysTick()
7. Add CLK\_SysTickLongDelay() for long delay
8. Fix UART\_SelectLINMode() clear enable bit setting bug
9. Fix PLL clock source selection bug in CLK\_SetCoreClock()
10. Fix clear Receive Line Status interrupt flag bug in UART\_ClearIntFlag().

### Revision 3.00.002 (Released 2014-12-23)

1. Fix PWM driver bug for output low when duty is 100%
2. Fix BPWM driver bug for output low when duty is 100%
3. Fix CLK driver bug in CLK\_SetCoreClock():
4. Fix CLK driver constant definitions error of (B)PWM0/1\_MODULE clock source selection.
5. Fix GPIO\_ENABLE\_DOUT\_MASK() and GPIO\_DISABLE\_DOUT\_MASK() bug of GPIO driver.
6. Fix PWM driver bug of PWM\_MASK\_OUTPUT() to remove redundant parenthesis.
7. Fix BPWM driver bug of BPWM\_MASK\_OUTPUT() to remove redundant parenthesis

8. Fix UART driver clear flag bug in UART\_ClearIntFlag()
9. Fix I2C driver module reset bug of I2C\_Close().
10. Fix clear RS-485 address byte detection flag bug in UART\_RS485\_CLEAR\_ADDR\_FLAG() of UART driver.
11. Fix clear RS-485 address byte detection flag clear bug in RS485\_HANDLE() of UART\_RS485\_Slave sample code.
12. Fix NVIC\_EnableIRQ() to NVIC\_DisableIRQ() after CHIP wake-up in I2C\_Wakeup\_Slave sample code
13. Fix UART RS485 RTS active level to high level active in RS485\_9bitModeMaster() of UART RS485 Sample code.
14. Add PWM\_EnableLoadMode() and PWM\_DisableLoadMode() functions to PWM driver
15. Add PWM\_SetBrakePinSource() function to PWM driver
16. Add CLK\_GetPCLKFreq() function to CLK driver
17. Add new macro PWM\_SET\_DEADZONE\_CLK\_SRC() to PWM driver
18. Add nonblocking printf implementation and use predefine compiler option to enable/disable it.

#### Revision 3.00.002 (Released 2014-07-18)

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1. Fixed constant definitions of Timer 2 and Timer 3 in clk.h
2. Fixed SYS\_Init() GPIO initial bug of \SampleCode\StdDriver\ACMP.

#### Revision 3.00.001 (Released 2014-06-12)

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1. First Release

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