

ARM926EJS

32-bit Microprocessor

NUC980 Non-OS BSP

Revision History

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Revision 1.05.000 (Released 2024-1-24)

1. Added LwIP_TCP_xxx, LwIP_UDP_xxx, and LwIP_TFTP_xxx sample codes running on FreeRTOS.
2. Added the EBI SRAM sample code.
3. Added GCC project in SPINAND_Yaffs2 sample code and fix kfree issue.
4. Fixed USBH mass storage driver get_max_lun() bug.
5. Added ADC touch function.
6. Minor bug fix.

Revision 1.04.000 (Released 2022-8-16)

1. Modified Eclipse installation guide of user manual.
2. Fixed bugs of CAN, Crypto AES, USBH, SDH, and FMI drivers.
3. Fixed GCC compilation error while there's space in directory name.
4. Enable I2C pin schmitt trigger.
5. Modify USBD initial flow.
6. Fix the bug that sysFiqHandler was not called by FIQ.

Revision 1.03.000 (Released 2020-9-14)

7. Added Apache-2.0 license declaration into driver source code.
8. Updated 64MB and 128MB DDR parameters.
9. Improved NuWriter performance.
10. Minor bug fix.

Revision 1.02.000 (Released 2019-12-18)

1. Made DMA buffers cache line aligned to avoid data corruption.
2. Fixed CAN driver bit time setting error.
3. Added EBI driver.
4. Minor bug fix.

Revision 1.01.000 (Released 2019-6-28)

1. Added Eclipse project support.
2. Minor bug fix.

Revision 1.00.000 (Released 2018-11-14)

1. Initial release.

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