



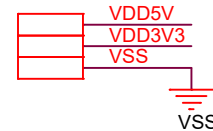
CON2			CON3		
PL4	1	2	VDD5V	1	2
PL5	3	4	VDD5V	3	4
PN14	5	6	VDD5V	5	6
PD15	7	8	VSS	7	8
PN12	9	10	PH3	9	10
PN13	11	12	PH2	11	12
PN7	13	14	PH1	13	14
PN6	15	16	PH0	15	16
PN5	17	18	PI15	17	18
PN4	19	20	PI14	19	20
PN3	21	22	PI13	21	22
PN2	23	24	PI12	23	24
PN1	25	26	PI11	25	26
PN0	27	28	PI10	27	28
VSS	29	30	PI9	29	30
PN10	31	32	PI8	31	32
VSS	33	34	PB15	33	34
PN11	35	36	PB14	35	36
VSS	37	38	PB13	37	38
PD9	39	40	PB12	39	40
PD8	41	42	PB11	41	42
PC4	43	44	PB10	43	44
PC5	45	46	PB9	45	46
PC0	47	48	PB8	47	48
VSS	49	50	PK3	49	50
PC1	51	52	PK2	51	52
VSS	53	54	PK1	53	54
PC2	55	56	PK0	55	56
PC3	57	58	PI7	57	58
PC7	59	60	PI6	59	60
PC6	61	62	PI5	61	62
VSS	63	64	PI4	63	64
E MDIP0	65	66	PI3	65	66
E MDIN0	67	68	PI2	67	68
VSS	69	70	PI1	69	70
E MDIP1	71	72	PI0	71	72
E MDIN1	73	74	PD13	73	74
VSS	75	76	PD12	75	76
E MDIP2	77	78	PD11	77	78
E MDIN2	79	80	PD10	79	80
VSS	81	82	PD9	81	82
E MDIP3	83	84	PD8	83	84
E MDIN3	85	86	PD7	85	86
VSS	87	88	PD6	87	88
F MDIP0	89	90	PD5	89	90
F MDIN0	91	92	PD4	91	92
VSS	93	94	PD3	93	94
F MDIP1	95	96	PD2	95	96
F MDIN1	97	98	VSS	97	98
VSS	99	100	PM2	99	100
F MDIP2	101	102	PM3	101	102
F MDIN2	103	104	PM4	103	104
VSS	105	106	PM5	105	106
F MDIP3	107	108	PM6	107	108
F MDIN3	109	110	PM7	109	110
VSS	111	112	PM8	111	112
PE15	113	114	PM9	113	114
PE14	115	116	PM10	115	116
HSUSB0_ID	117	118	PM11	117	118
PF15	119	120	VSS	119	120

DF40C-120DS-0.4V (51) (Pitch 0.4mm, female)

CON2			CON3		
PL4	1	2	VDD5V	1	2
PL5	3	4	VDD5V	3	4
PN14	5	6	VDD5V	5	6
PD15	7	8	VSS	7	8
PN12	9	10	PH3	9	10
PN13	11	12	PH2	11	12
PN7	13	14	PH1	13	14
PN6	15	16	PH0	15	16
PN5	17	18	PI15	17	18
PN4	19	20	PI14	19	20
PN3	21	22	PI13	21	22
PN2	23	24	PI12	23	24
PN1	25	26	PI11	25	26
PN0	27	28	PI10	27	28
VSS	29	30	PI9	29	30
PN10	31	32	PI8	31	32
VSS	33	34	PB15	33	34
PN11	35	36	PB14	35	36
VSS	37	38	PB13	37	38
PD9	39	40	PB12	39	40
PD8	41	42	PB11	41	42
PC4	43	44	PB10	43	44
PC5	45	46	PB9	45	46
PC0	47	48	PB8	47	48
VSS	49	50	PK3	49	50
PC1	51	52	PK2	51	52
VSS	53	54	PK1	53	54
PC2	55	56	PK0	55	56
PC3	57	58	PI7	57	58
PC7	59	60	PI6	59	60
PC6	61	62	PI5	61	62
VSS	63	64	PI4	63	64
E MDIP0	65	66	PI3	65	66
E MDIN0	67	68	PI2	67	68
VSS	69	70	PI1	69	70
E MDIP1	71	72	PI0	71	72
E MDIN1	73	74	PD13	73	74
VSS	75	76	PD12	75	76
E MDIP2	77	78	PD11	77	78
E MDIN2	79	80	PD10	79	80
VSS	81	82	PD9	81	82
E MDIP3	83	84	PD8	83	84
E MDIN3	85	86	PD7	85	86
VSS	87	88	PD6	87	88
F MDIP0	89	90	PD5	89	90
F MDIN0	91	92	PD4	91	92
VSS	93	94	PD3	93	94
F MDIP1	95	96	PD2	95	96
F MDIN1	97	98	VSS	97	98
VSS	99	100	PM2	99	100
F MDIP2	101	102	PM3	101	102
F MDIN2	103	104	PM4	103	104
VSS	105	106	PM5	105	106
F MDIP3	107	108	PM6	107	108
F MDIN3	109	110	PM7	109	110
VSS	111	112	PM8	111	112
PE15	113	114	PM9	113	114
PE14	115	116	PM10	115	116
HSUSB0_ID	117	118	PM11	117	118
PF15	119	120	VSS	119	120

DF40C-120DS-0.4V (51) (Pitch 0.4mm, female)

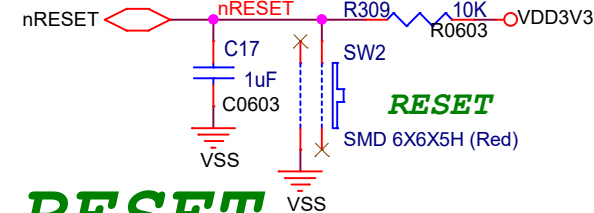
VDD5V  
VDD3V3  
VSS



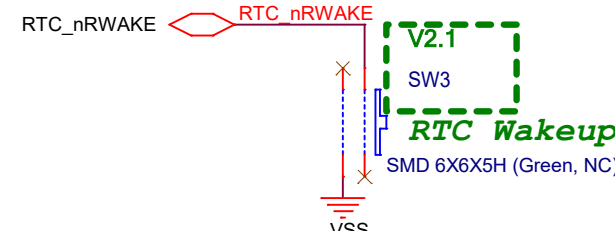
## Power

PA[14:0]	PA[14:0]
PB[15:8]	PB[15:8]
PC[7:0]	PC[7:0]
PC[15:12]	PC[15:12]
PD[5:0]	PD[5:0]
PD[15:8]	PD[15:8]
PE[15:14]	PE[15:14]
PF[15:14]	PF[15:14]
PG[15:0]	PG[15:0]
PH[9:0]	PH[9:0]
PH[15:12]	PH[15:12]
PI[15:0]	PI[15:0]
PJ[15:12]	PJ[15:12]
PK[15:0]	PK[15:0]
PL[15:0]	PL[15:0]
PM[15:0]	PM[15:0]
PN[7:0]	PN[7:0]
PN[15:10]	PN[15:10]

## GPIO



## RESET



## RTC Wakeup

HSUSB0_ID	HSUSB0_ID
HSUSB0_D+	HSUSB0_D+
HSUSB0_D-	HSUSB0_D-
HSUSB1_D+	HSUSB1_D+
HSUSB1_D-	HSUSB1_D-

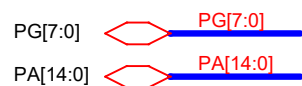
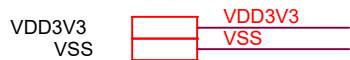
## HSUSB

EADC0_CH[7:0]	EADC0_CH[7:0]
E_MDIP[3:0]	E_MDIP[3:0]
E_MDIN[3:0]	E_MDIN[3:0]
E_LED[2:0]	E_LED[2:0]
F_MDIP[3:0]	F_MDIP[3:0]
F_MDIN[3:0]	F_MDIN[3:0]
F_LED[2:0]	F_LED[2:0]

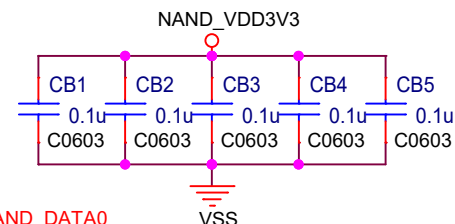
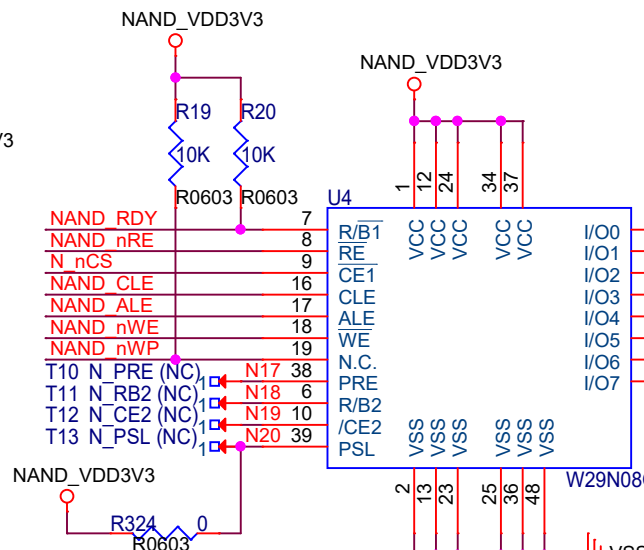
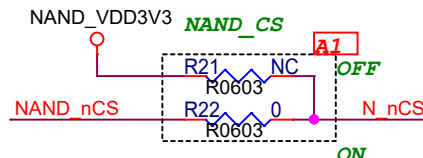
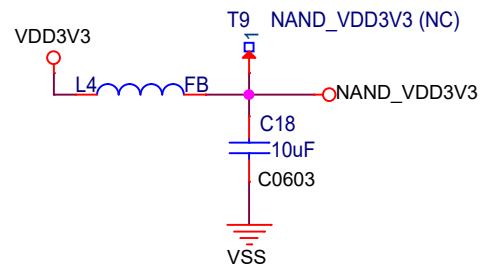
## Ethernet

**nuvoTon Technology Corp.**

Title		
NuMaker_MA35D1_Base		
Size A	Document Number	Rev V2.1
SOM Connectors		
Date:	Tuesday, January 31, 2023	Sheet 2 of 22



PA0	NAND_DATA0
PA1	NAND_DATA1
PA2	NAND_DATA2
PA3	NAND_DATA3
PA4	NAND_DATA4
PA5	NAND_DATA5
PA6	NAND_DATA6
PA7	NAND_DATA7
PA8	NAND_RDY
PA9	NAND_nRE
PA10	NAND_nWE
PA11	NAND_CLE
PA12	NAND_ALE
PA13	NAND_nCS
PA14	NAND_nWP



## NAND Flash

Note: These GPIO PA0~PA14 and PG0~PG7 pins belong to I/O group 1 (VDDIO1) and the default input voltage of VDDIO1 is 3.3V on SOM board.

## Power-on Setting

PG0	Secure Boot
L	Secure Boot Enable
H	Secure Boot Disable

PG1	Boot Source QSPI0, SD/eMMC I/O Voltage
L	3.3V
H	1.8V

PG3	PG2	Boot Source
L	L	QSPI0 Flash
L	H	SD/eMMC
H	L	NAND Flash
H	H	USB

PG7	PG6	Bootting from QSPI0
L	L	SPI-NAND, 1-bit
H	L	SPI-NOR, 1-bit

PG6	Bootting from SD/eMMC
L	SD0/eMMC0 bootting
H	SD1/eMMC1 bootting

PG7	Bootting from SD/eMMC
L	eMMC 4-bit bootting
H	eMMC 8-bit bootting

PG5	PG4	Bootting from NAND
L	L	Ignore
L	H	NAND flash page 2KB
H	L	NAND flash page 4KB
H	H	NAND flash page 8KB

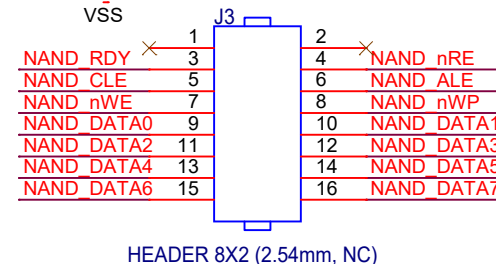
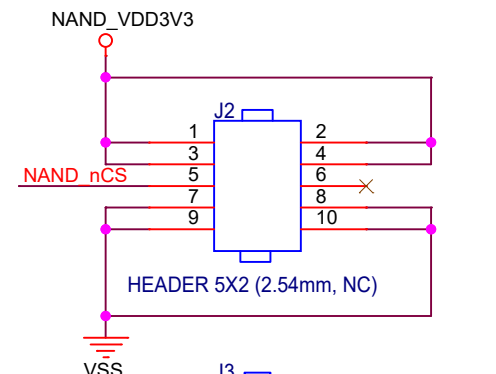
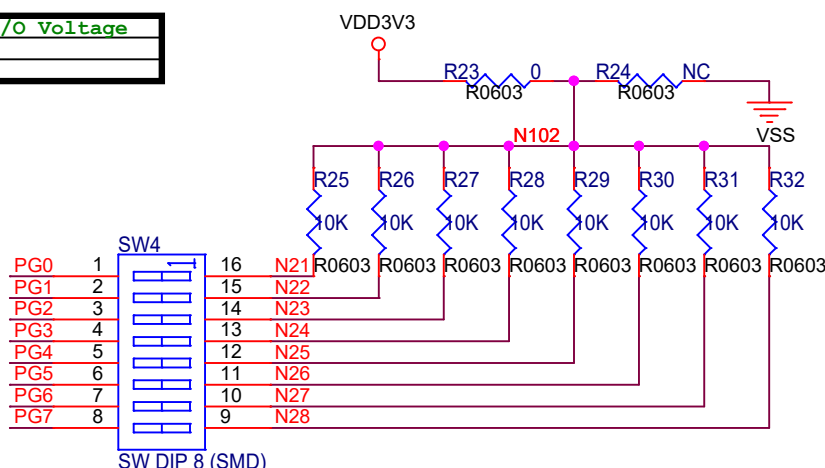
PG7	PG6	Bootting from NAND
L	L	Ignore
L	H	BCH T12
H	L	BCH T24
H	H	NO ECC

PG4	Bootting from USB
L	USBD bootting
H	USBH bootting

PG5	Bootting from USBH
L	USBH port 0 bootting
H	USBH port 1 bootting

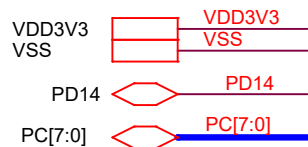
PG6	Bootting from USBH
L	Over-current low-active detect
H	Over-current high-active detect

Note: These GPIO PG0~PG7 pins are internal pull-low inside MA35D1 chip.

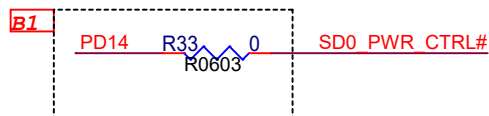


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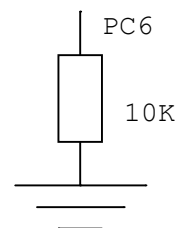
Title		
NuMaker_MA35D1_Base		
Size A	Document Number	Rev V2.1
Power-on Setting and NAND Flash		
Date:	Monday, January 30, 2023	Sheet 3 of 22



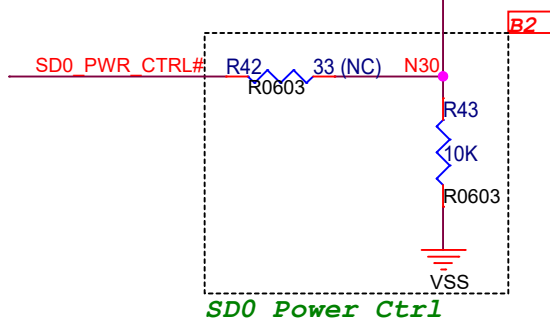
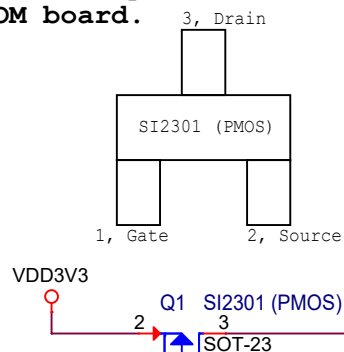
PC0	R34	33	R0402	SD0_CMD
PC1	R35	33	R0402	SD0_CLK
PC2	R36	33	R0402	SD0_DAT0
PC3	R37	33	R0402	SD0_DAT1
PC4	R38	33	R0402	SD0_DAT2
PC5	R39	33	R0402	SD0_DAT3
PC6	R40	33	R0402	SD0_nCD
PC7	R41	33	R0402	SD0_WP



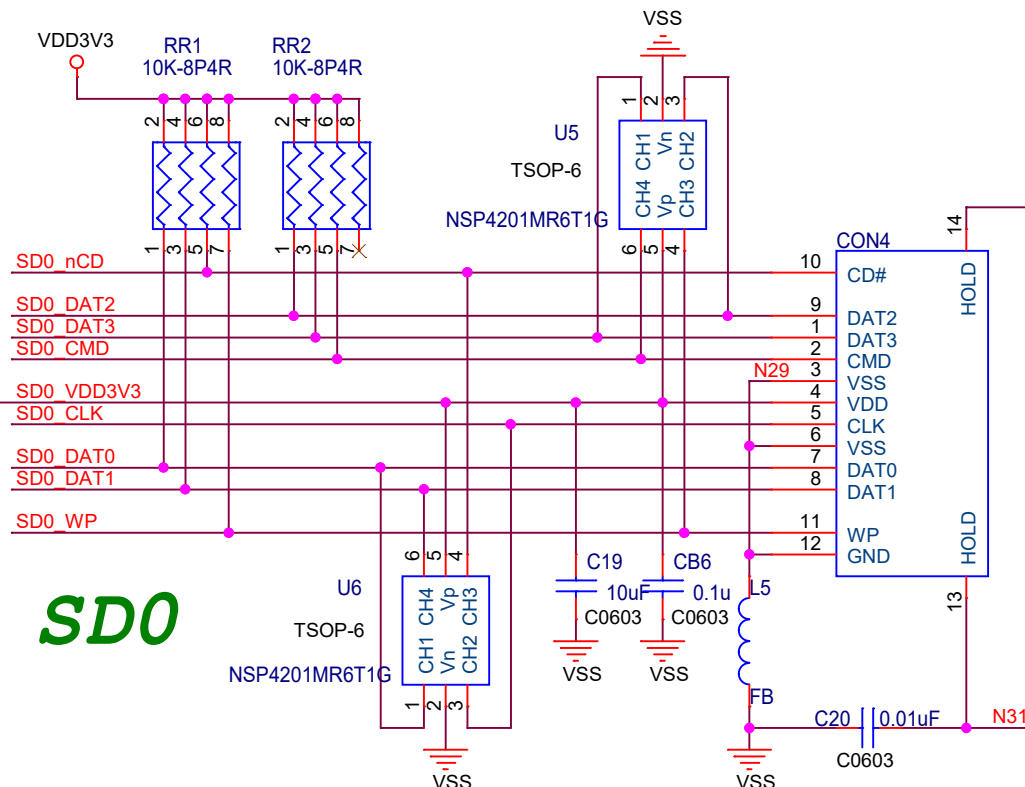
Note: If these PC0~PC5 pins (eMMC0\_xxx) are used to connect with eMMC device and act as the booting source, please pulls the PC6 pin (SD0\_nCD) to low.



Note: These GPIO PC0~PC7 pins belong to I/O group 6 (VDDIO6) and the default input voltage of VDDIO6 is 3.3V on SOM board.



Note: The power of SD0\_VDD3V3 is always ON (uncontrollable) by default. (R42 is NC on this BASE board by default)



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Title

NuMaker\_MA35D1\_Base

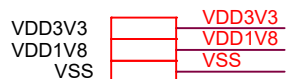
Size A

Document Number  
SD0

Rev V2.1

Date: Thursday, March 16, 2023

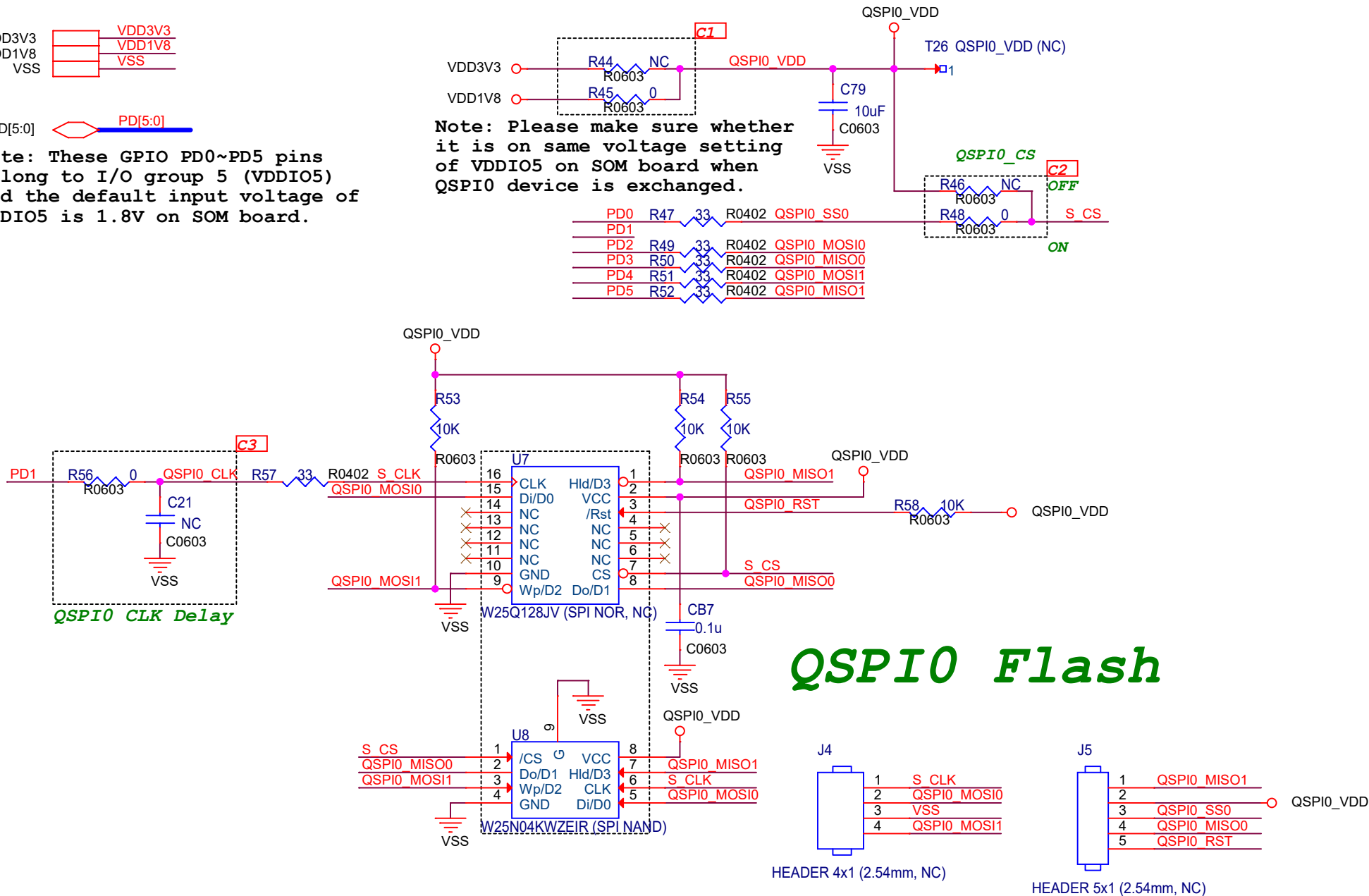
Sheet 4 of 22



Note: These GPIO PD0~PD5 pins belong to I/O group 5 (VDDIO5) and the default input voltage of VDDIO5 is 1.8V on SOM board.

Note: Please make sure whether it is on same voltage setting of VDDIO5 on SOM board when QSPI0 device is exchanged.

PD0	R47	33	R0402	QSPI0 SS0
PD1				
PD2	R49	33	R0402	QSPI0 MOSI0
PD3	R50	33	R0402	QSPI0 MISO0
PD4	R51	33	R0402	QSPI0 MOSI1
PD5	R52	33	R0402	QSPI0 MISO1



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Title <b>NuMaker_MA35D1_Base</b>		
Size A	Document Number <b>QSPI0</b>	Rev V2.1
Date: Monday, January 30, 2023	Sheet 5	of 22

VSS

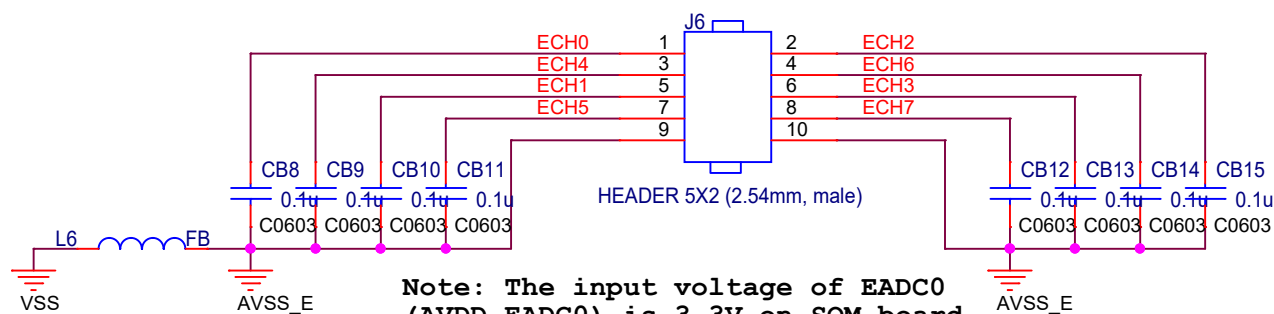
VSS

EADC0\_CH[7:0]

EADC0\_CH[7:0]

EADC0_CH0	R59	33	R0402	ECH0
EADC0_CH4	R60	33	R0402	ECH4
EADC0_CH1	R61	33	R0402	ECH1
EADC0_CH5	R62	33	R0402	ECH5
EADC0_CH2	R63	33	R0402	ECH2
EADC0_CH6	R64	33	R0402	ECH6
EADC0_CH3	R65	33	R0402	ECH3
EADC0_CH7	R66	33	R0402	ECH7

## EADC0



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Title

NuMaker\_MA35D1\_Base

Size  
A

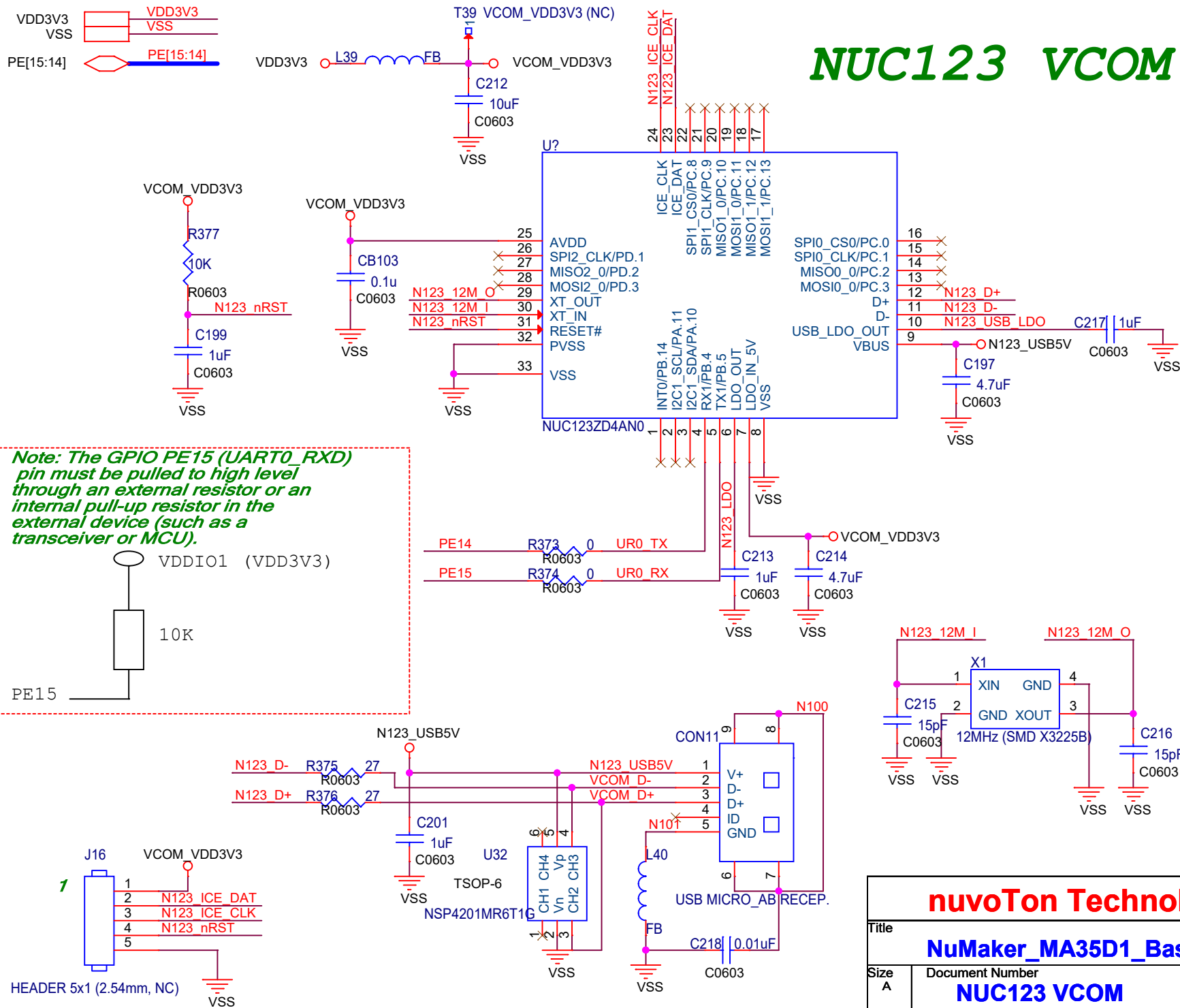
Document Number  
EADC0

Rev  
V2.1

Date: Monday, January 30, 2023

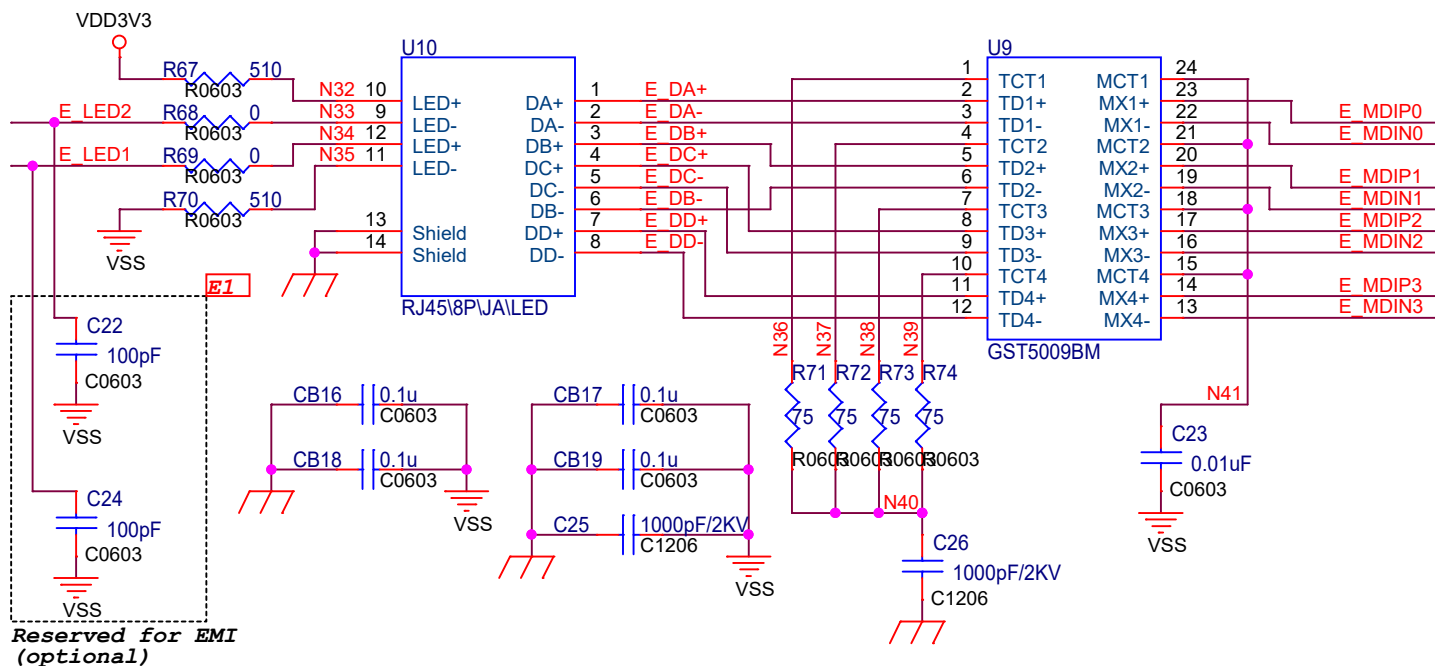
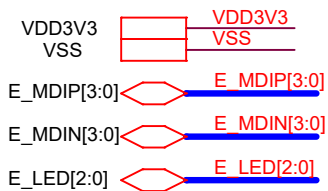
Sheet 6 of 22

# NUC123 VCOM



**nuvoTon Technology Corp.**

Title		
NuMaker_MA35D1_Base		
Size	Document Number	Rev
A	NUC123 VCOM	V2.1
Date:	Tuesday, May 21, 2024	Sheet 7 of 14



**nuvoTon Technology Corp.**

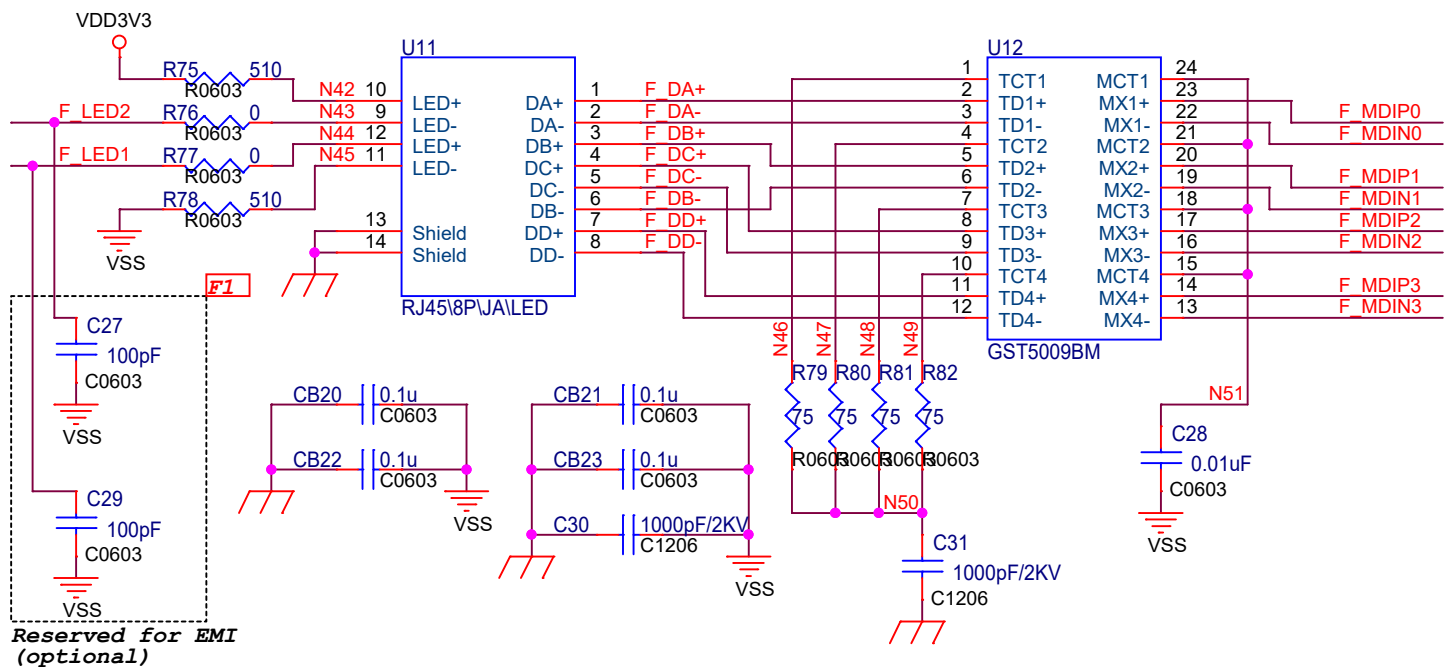
Title  
**NuMaker\_MA35D1\_Base**

Size A Document Number  
**Ethernet 0 (PE)**

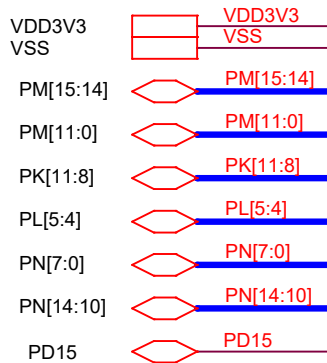
Rev  
V2.1

Date: Monday, January 30, 2023 Sheet 7 of 22

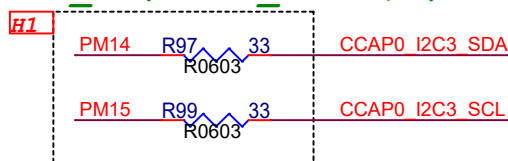




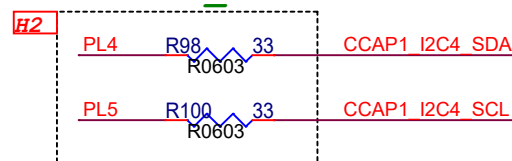




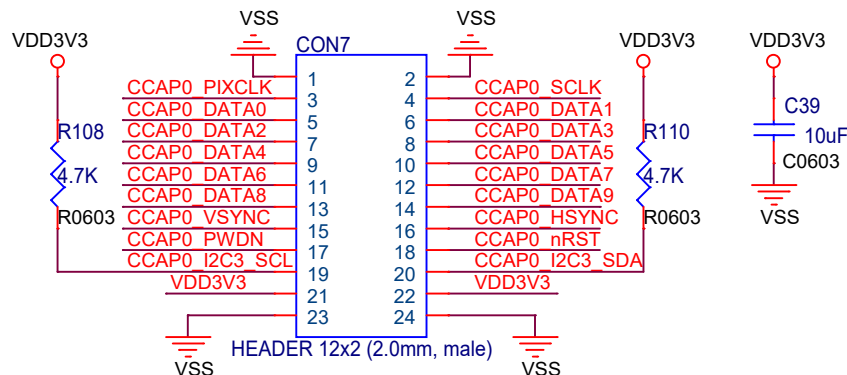
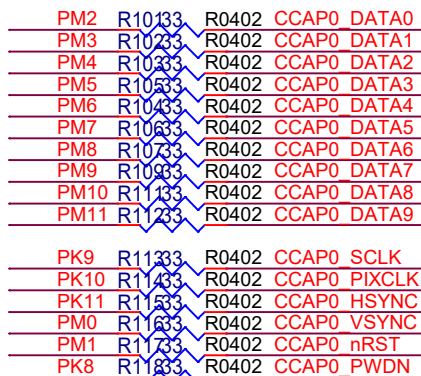
## I2C\_3 (TRACE\_DATA2/3)



## I2C\_4

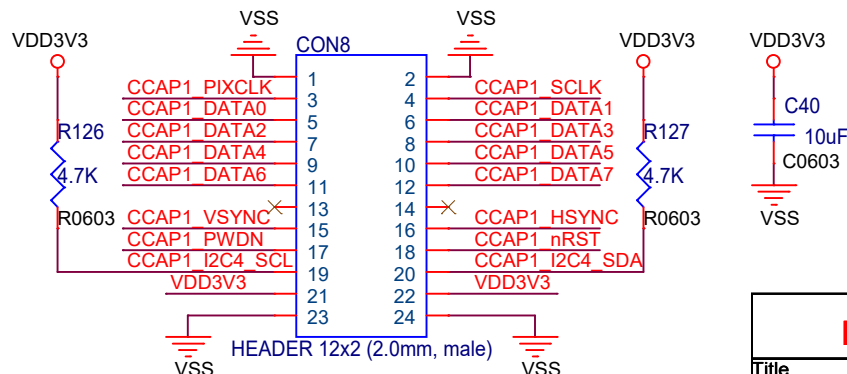
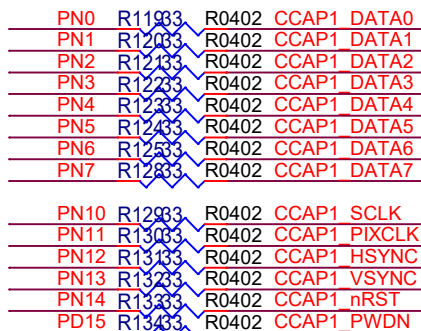


# CCAP0 Connector



Note: These GPIO PK8~PK11 and PM0~PM11 pins belong to I/O group 2 (VDDIO2) and the default input voltage of VDDIO2 is 3.3V on SOM board.

# CCAP1 Connector



Note: These GPIO PN0~PN14 pins belong to I/O group 7 (VDDIO7) and the GPIO PD15 belong to I/O group 1 (VDDIO1), these default input voltages of VDDIO7 and VDDIO1 are 3.3V on SOM board.

**nuvoTon Technology Corp.**

Title		
NuMaker_MA35D1_Base		
Size A	Document Number	Rev V2.1
CCAP 0/1 Connectors		
Date:	Monday, January 30, 2023	Sheet 10 of 22

VDD5V  
VDD3V3  
VSS

nRESET

PM12

PD12

PJ[13:12]

PB[15:10]

PG[10:8]

PK[7:4]

PI[15:8]

PH[7:0]

PC[15:12]

PH[15:12]

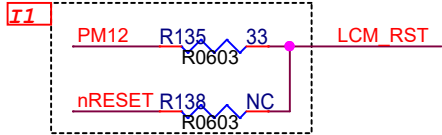
I2C1\_SDA

I2C1\_SCL

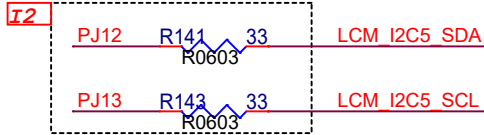
**MPU6500 I2C1\_SDA/SCL**

PG8	R1533	R0402	LCM_VSYNC
PG9	R14733	R0402	LCM_HSYNC
PG10	R14833	R0402	LCM_CLK
PK4	R14933	R0402	LCM_DEN
PK5	R15033	R0402	LCM_PWM
PK6	R15133	R0402	LCM_CS
PK7	R15233	R0402	LCM_BLEn
PH8	R15333	R0402	LCM_DATA0
PH9	R15433	R0402	LCM_DATA1
PH10	R15533	R0402	LCM_DATA2
PH11	R15633	R0402	LCM_DATA3
PH12	R15733	R0402	LCM_DATA4
PH13	R15833	R0402	LCM_DATA5
PH14	R15933	R0402	LCM_DATA6
PH15	R16033	R0402	LCM_DATA7
PH0	R16133	R0402	LCM_DATA8
PH1	R16233	R0402	LCM_DATA9
PH2	R16333	R0402	LCM_DATA10
PH3	R16433	R0402	LCM_DATA11
PH4	R16533	R0402	LCM_DATA12
PH5	R16633	R0402	LCM_DATA13
PH6	R16733	R0402	LCM_DATA14
PH7	R16833	R0402	LCM_DATA15
PC12	R16933	R0402	LCM_DATA16
PC13	R17033	R0402	LCM_DATA17
PC14	R17133	R0402	LCM_DATA18
PC15	R17233	R0402	LCM_DATA19
PH12	R17333	R0402	LCM_DATA20
PH13	R17433	R0402	LCM_DATA21
PH14	R17533	R0402	LCM_DATA22
PH15	R17633	R0402	LCM_DATA23

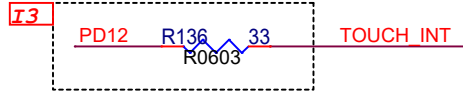
**LCM\_RST (TRACE DATA0)**



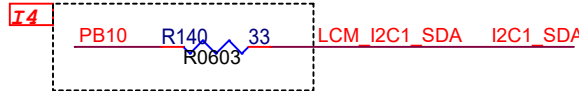
**I2C\_5 (EBI\_ADR12/13)**



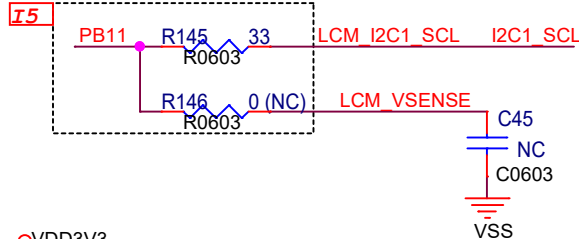
**TOUCH\_INT (EBI\_nCS1)**



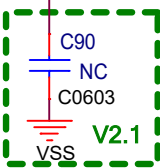
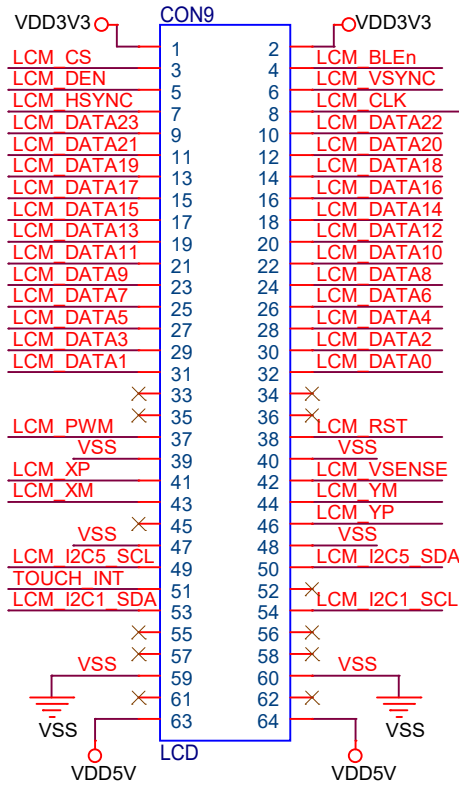
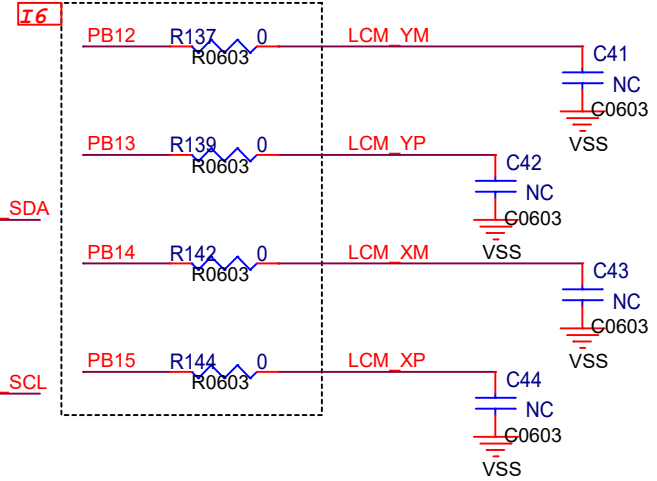
**I2C1\_SDA (EBI\_MCLK)**



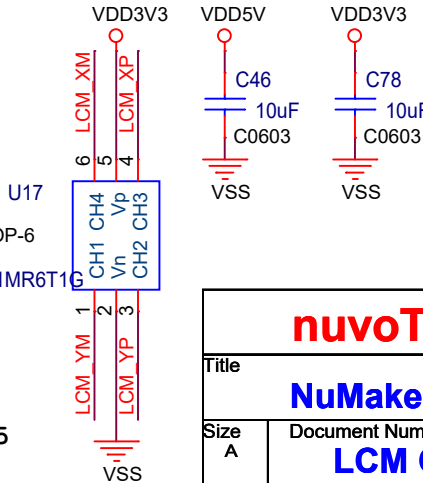
**I2C1\_SCL (LCM\_VSENSE)**



**LCM Touch**

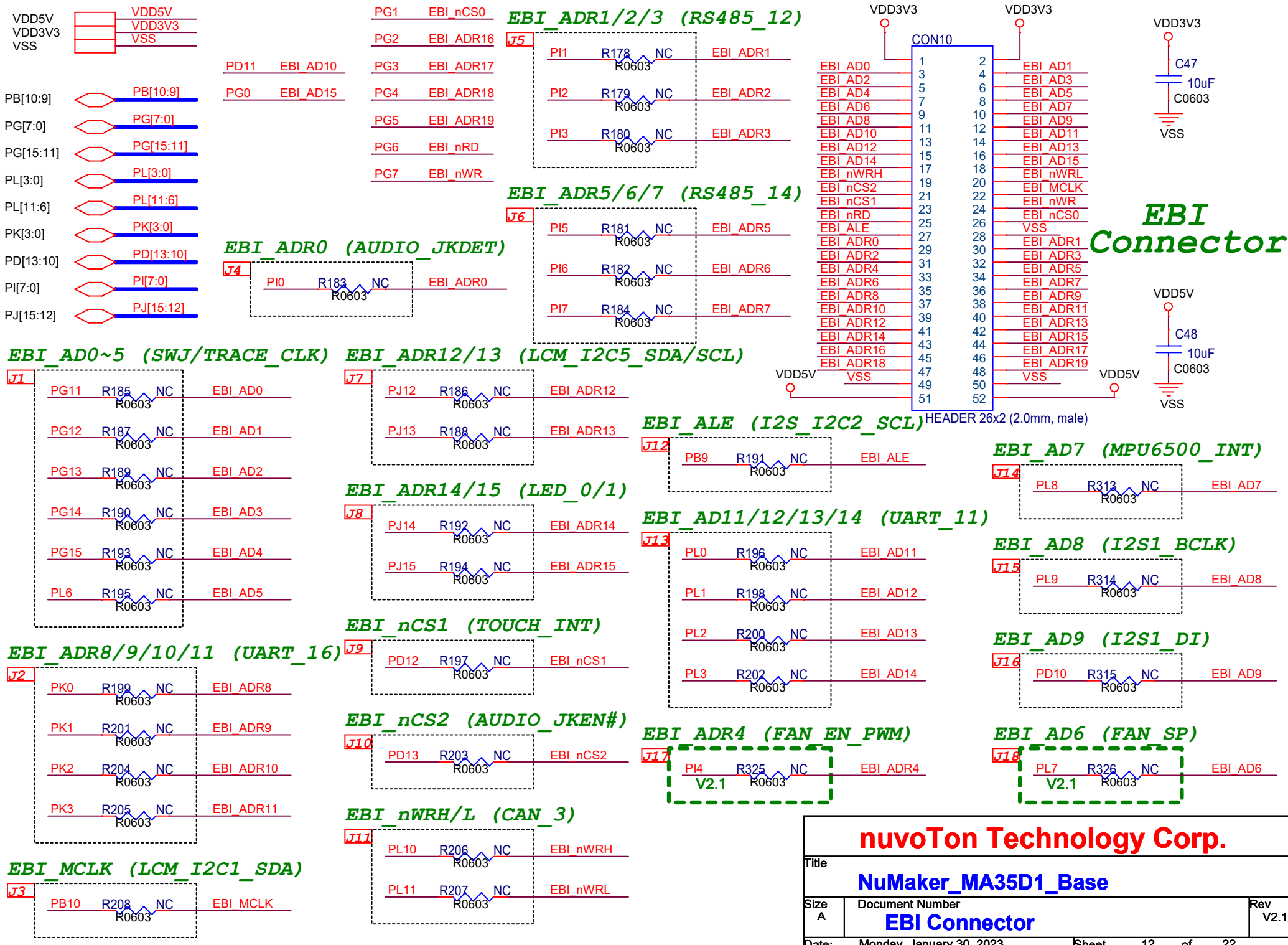


**LCM Connector**



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A	LCM Connector	V2.1
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Note: These GPIO PG8~PG10, PK4~PK7, PI8~PI15, PH0~PH7, PC12~PC15 and PH12~PH15 pins belong to I/O group 4 (VDDIO4), and the default input voltage of VDDIO4 is 3.3V on SOM board.



**Microphone**

**Codec**

**Speaker**

**Earphone**

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**NuMaker\_MA35D1\_Base**

**NAU88C22 Audio Codec**

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**Microphone**

**Speaker**

**Earphone**

**Codec**

**I2C\_2 (EBI\_ALE)**

**I2S\_0 (SC\_0)**

**AUDIO\_JKEN# (EBI\_nCS2)**

**AUDIO\_JKDET (EBI\_ADR0)**

**nuvoTon Technology Corp.**

**Title**

**NuMaker\_MA35D1\_Base**

**Size A**

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**Microphone**

**Speaker**

**Earphone**

**Codec**

**I2C\_2 (EBI\_ALE)**

**I2S\_0 (SC\_0)**

**AUDIO\_JKEN# (EBI\_nCS2)**

**AUDIO\_JKDET (EBI\_ADR0)**

**nuvoTon Technology Corp.**

**Title**

**NuMaker\_MA35D1\_Base**

**Size A**

**Document Number**

**NAU88C22 Audio Codec**

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**Microphone**

**Speaker**

**Earphone**

**Codec**

**I2C\_2 (EBI\_ALE)**

**I2S\_0 (SC\_0)**

**AUDIO\_JKEN# (EBI\_nCS2)**

**AUDIO\_JKDET (EBI\_ADR0)**

**nuvoTon Technology Corp.**

**NuMaker\_MA35D1\_Base**

**NAU88C22 Audio Codec**

**Title**

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**Microphone**

**Speaker**

**Earphone**

**Codec**

**I2C\_2 (EBI\_ALE)**

**I2S\_0 (SC\_0)**

**AUDIO\_JKEN# (EBI\_nCS2)**

**AUDIO\_JKDET (EBI\_ADR0)**

**nuvoTon Technology Corp.**

**NuMaker\_MA35D1\_Base**

**NAU88C22 Audio Codec**

**Title**

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**Microphone**

**Speaker**

**Earphone**

**Codec**

**I2C\_2 (EBI\_ALE)**

**I2S\_0 (SC\_0)**

**AUDIO\_JKEN# (EBI\_nCS2)**

**AUDIO\_JKDET (EBI\_ADR0)**

**nuvoTon Technology Corp.**

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**NAU88C22 Audio Codec**

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[illegible][illegible][illegible]

**Microphone**

**Speaker**

**Earphone**

**Codec**

**I2C\_2 (EBI\_ALE)**

**I2S\_0 (SC\_0)**

**AUDIO\_JKEN# (EBI\_nCS2)**

**AUDIO\_JKDET (EBI\_ADR0)**

**SPK OUT**

**Title**  
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**Microphone**

**Speaker**

**Earphone**

**Codec**

**Audio\_JKEN# (EBI\_nCS2)**

**Audio\_JKDET (EBI\_ADR0)**

**I2C\_2 (EBI\_ALE)**

**I2S\_0 (SC\_0)**

**nuvoTon Technology Corp.**

**Title**

**NuMaker\_MA35D1\_Base**

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**Document Number**

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**Microphone**

**Speaker**

**Earphone**

**Codec**

**I2C\_2 (EBI\_ALE)**

**I2S\_0 (SC\_0)**

**AUDIO\_JKEN# (EBI\_nCS2)**

**AUDIO\_JKDET (EBI\_ADR0)**

**Title**  
**NuMaker\_MA35D1\_Base**

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[illegible]

**Microphone**

**Speaker**

**Earphone**

**Codec**

**I2C\_2 (EBI\_ALE)**

**I2S\_0 (SC\_0)**

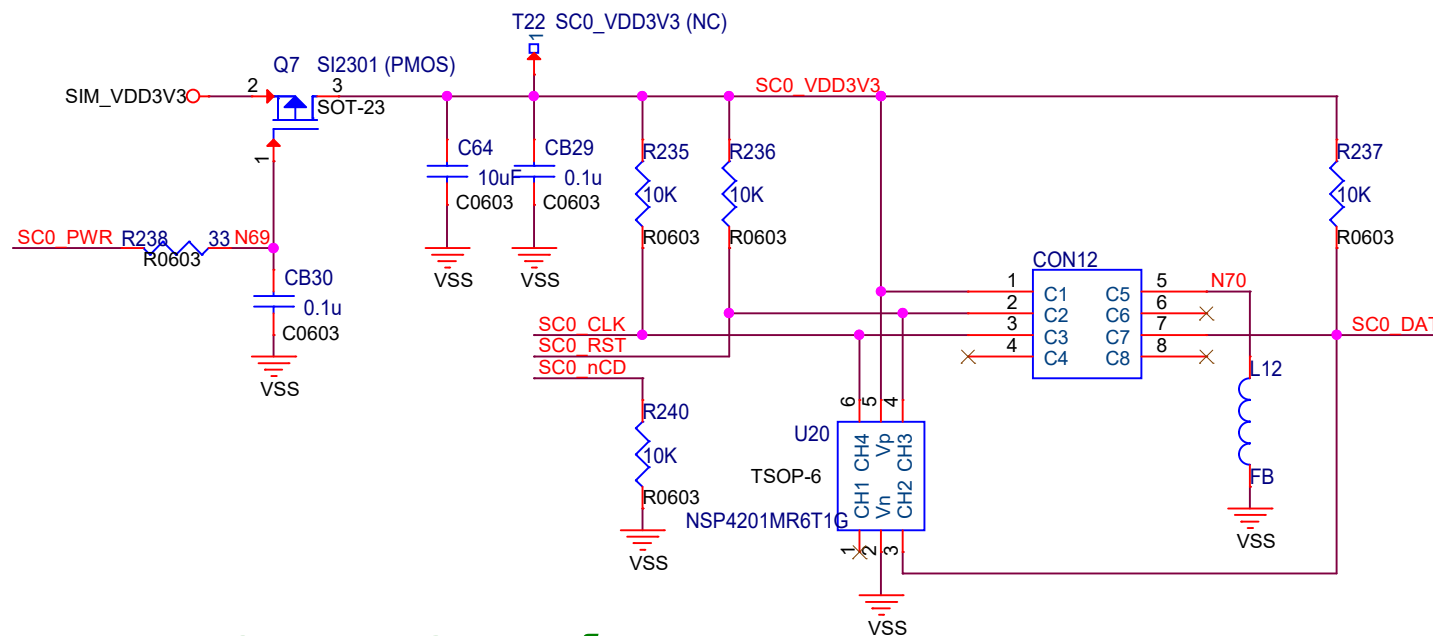
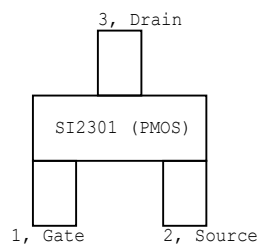
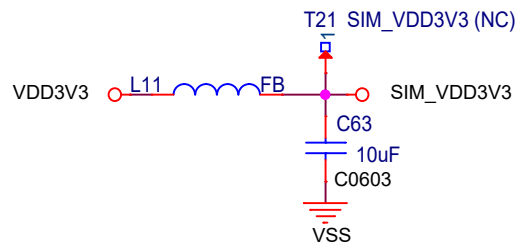
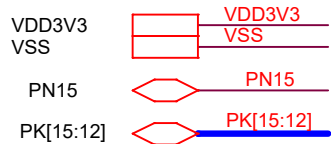
**AUDIO\_JKEN# (EBI\_nCS2)**

**AUDIO\_JKDET (EBI\_ADR0)**

**Title**  
**NuMaker\_MA35D1\_Base**

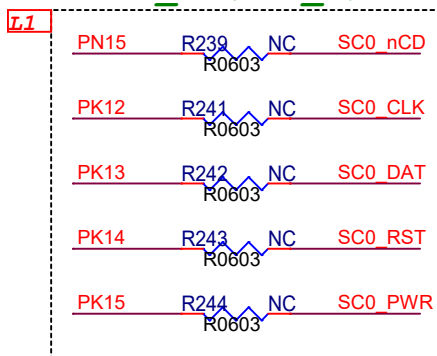
**Size A**  
**Document Number**  
**NAU88C22 Audio Codec**

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*SIM Card*

*SC\_0 (I2S\_0)*



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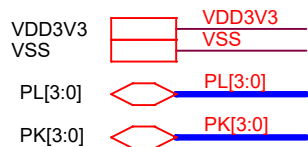
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**NuMaker\_MA35D1\_Base**

Size  
A Document Number  
**SIM Card**

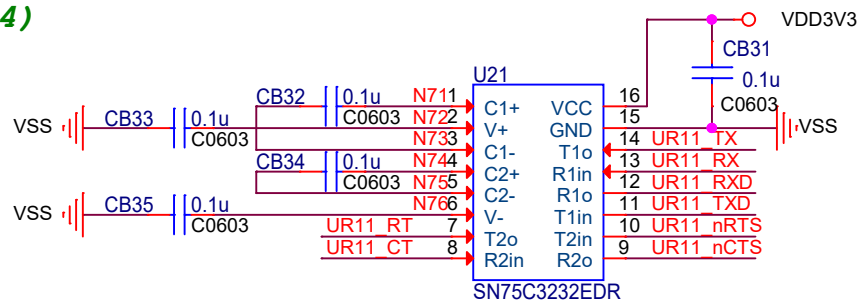
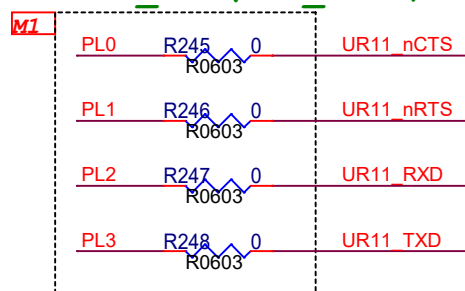
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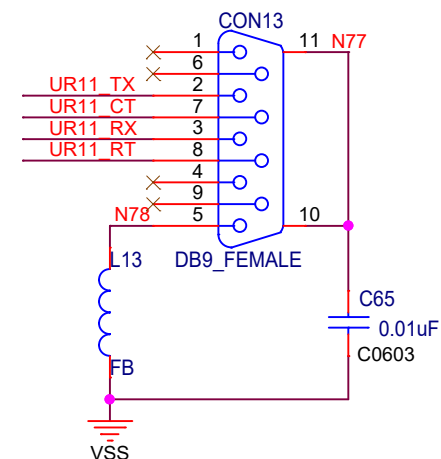




## UART\_11 (EBI AD11/12/13/14)

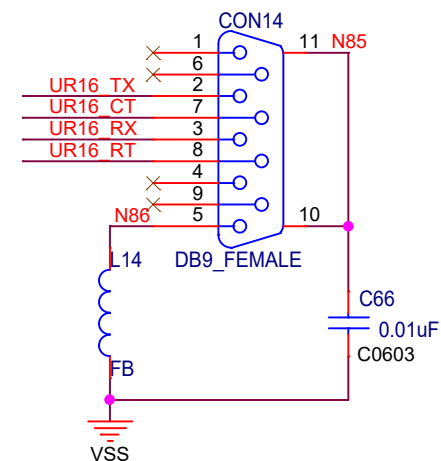
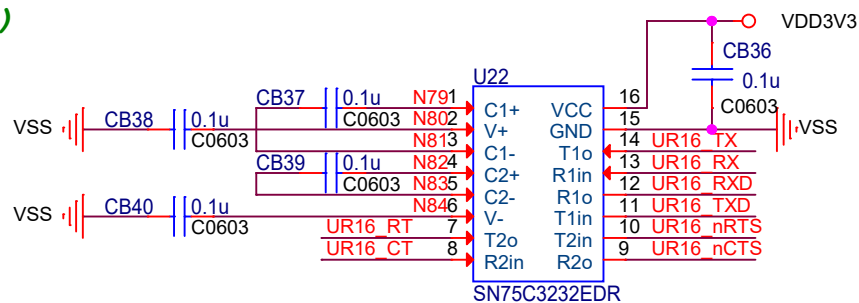
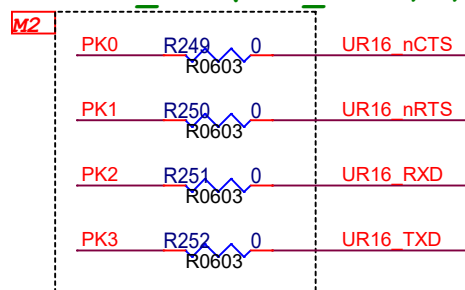


## RS232\_11



## RS232\_16

## UART\_16 (EBI ADR8/9/10/11)



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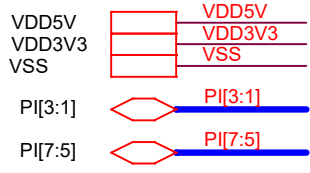
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**NuMaker\_MA35D1\_Base**

Size A Document Number  
**RS232**

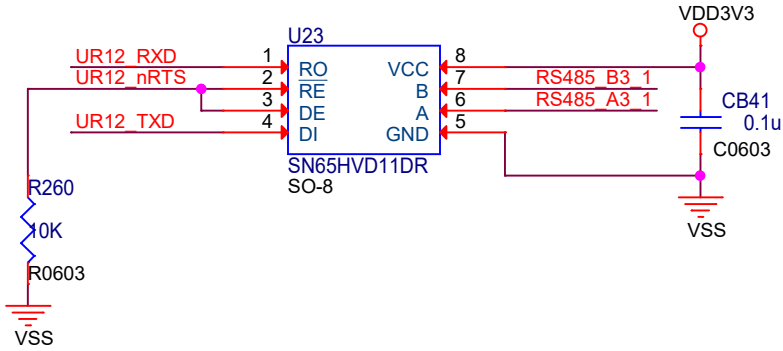
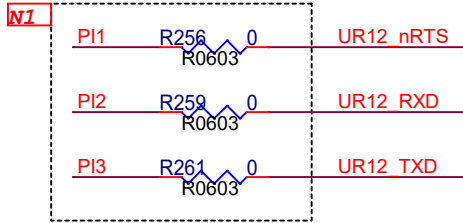
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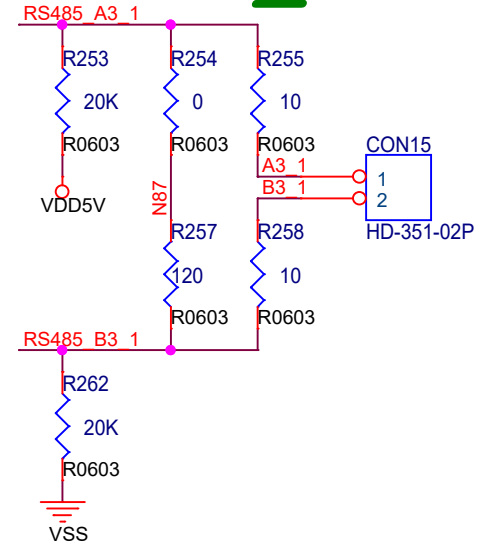




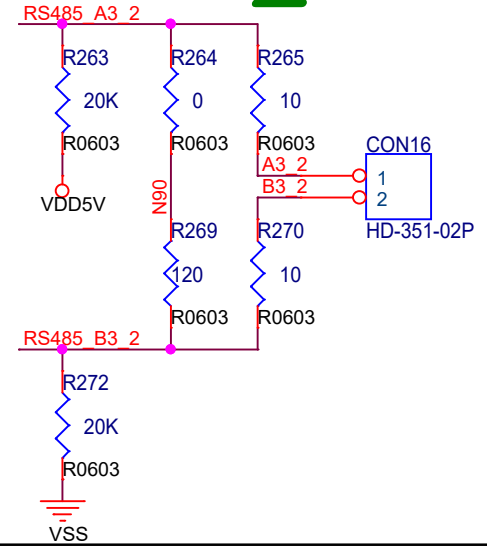
## RS485\_12 (EBI\_ADR1/2/3)



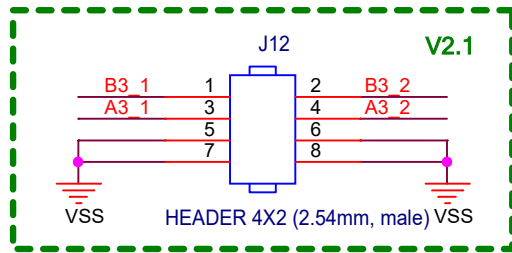
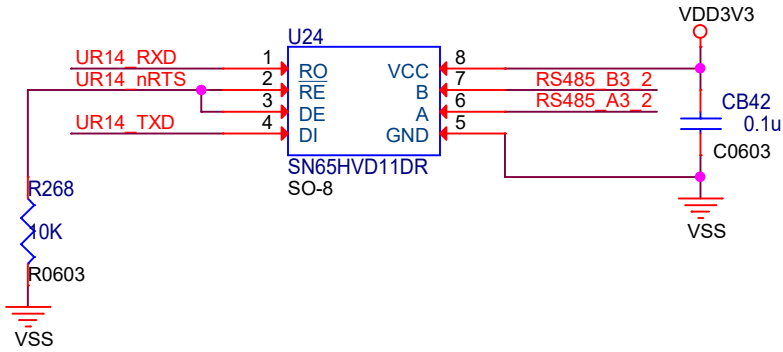
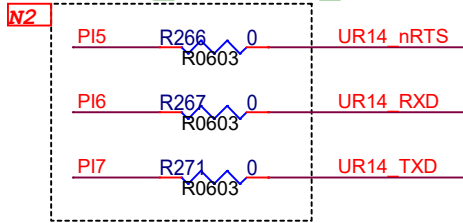
## RS485\_12



## RS485\_14

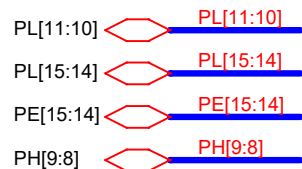
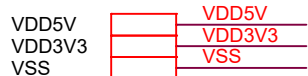


## RS485\_14 (EBI\_ADR5/6/7)

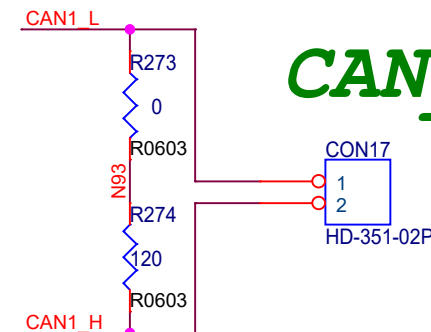
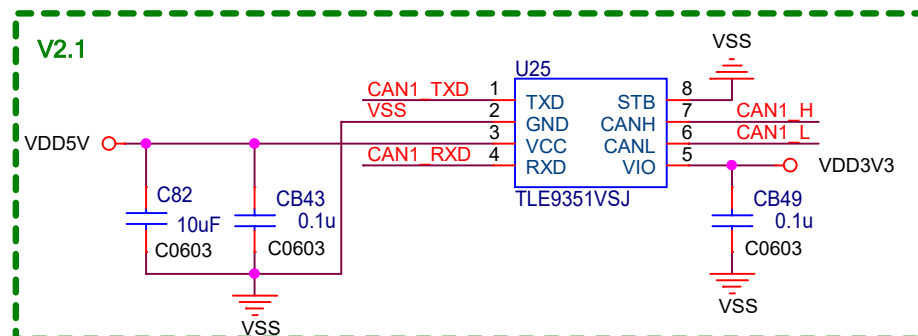
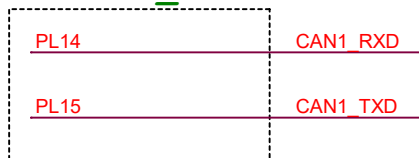


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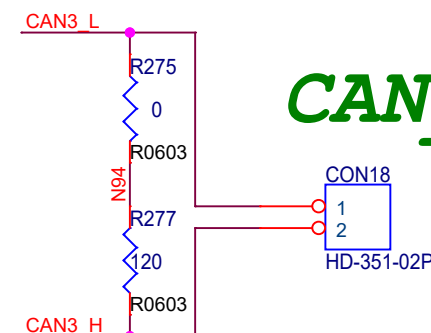
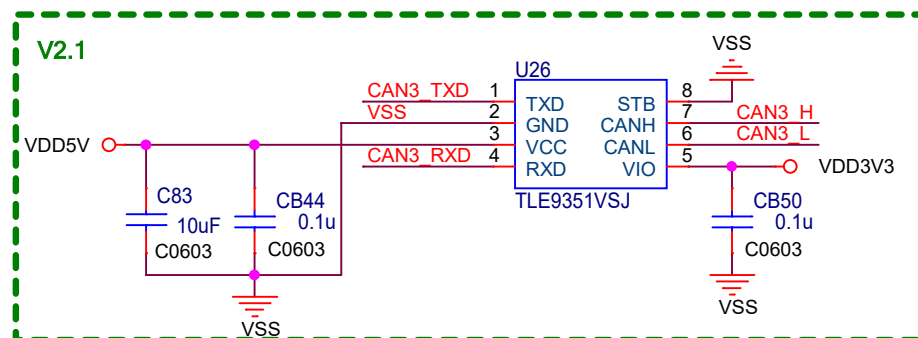
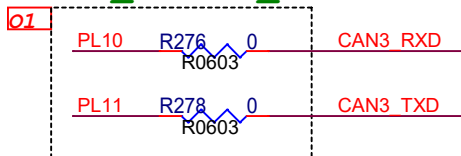


## CAN\_1



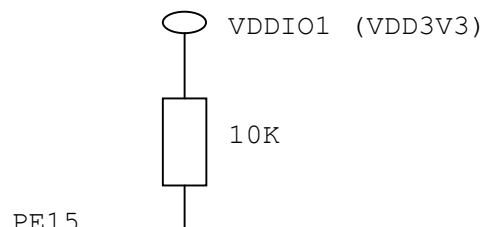
## CAN\_1

## CAN\_3 (EBI\_nWRH/L)



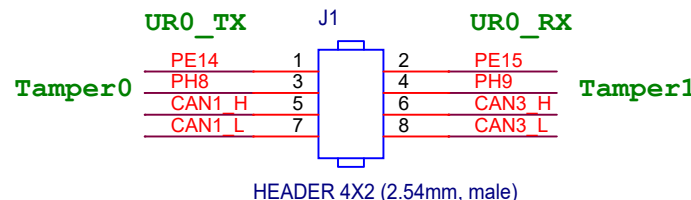
## CAN\_3

*Note: The GPIO PE15 (UART0\_RXD) pin must be pulled to high level through an external resistor or an internal pull-up resistor in the external device (such as a transceiver or MCU).*



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## UART0 & Tamper



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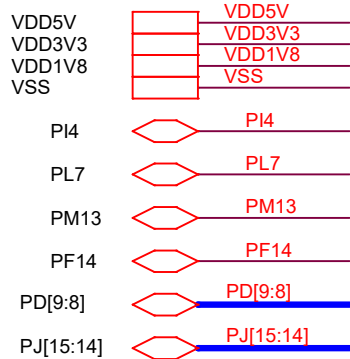
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**NuMaker\_MA35D1\_Base**

Size  
A Document Number  
**CAN FD**

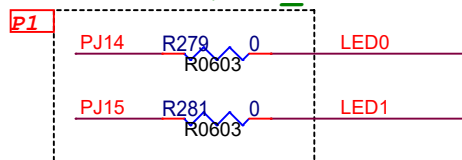
Rev  
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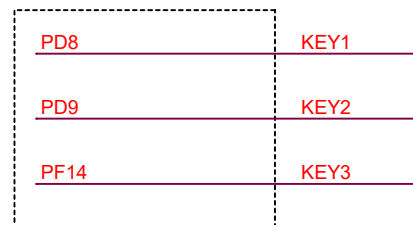
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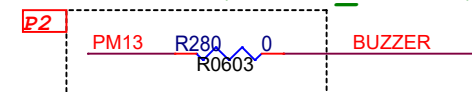
## LED0/1 (EBI\_ADR14/15)



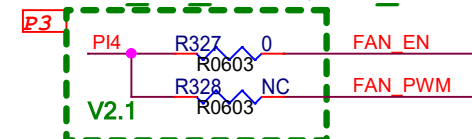
## KEY1/2/3



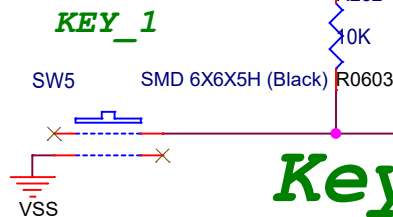
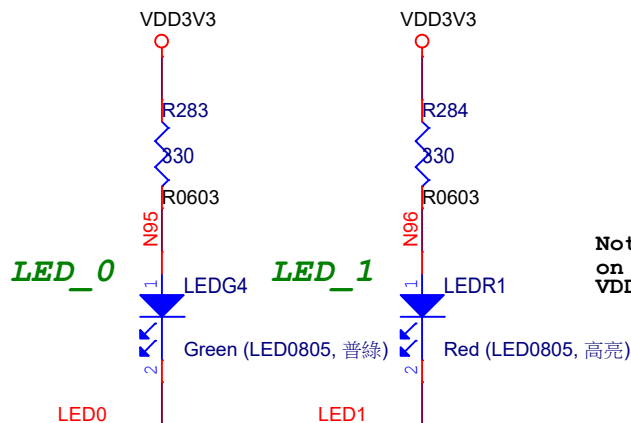
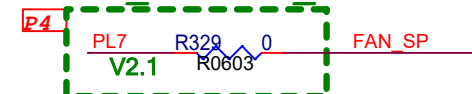
## Buzzer (TRACE\_DATA1)



## FAN EN PWM (EBI\_ADR4)

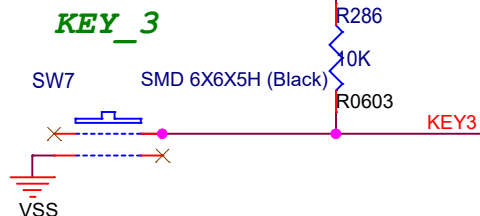
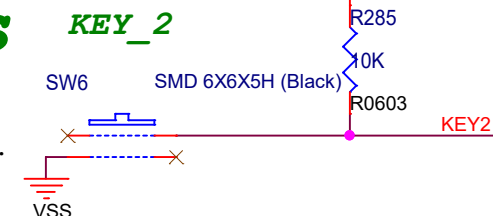


## FAN SP (EBI\_AD6)

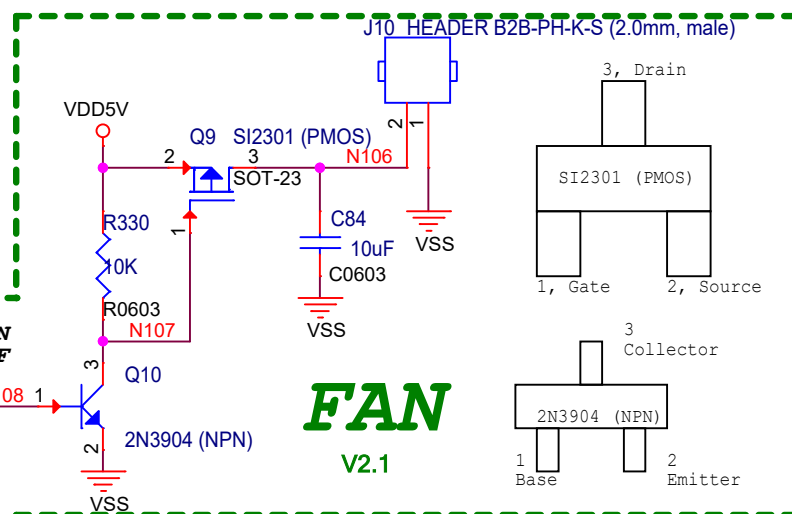
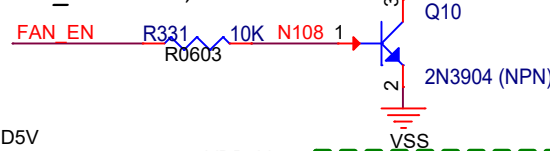


## Key Buttons

Note: PF14 I/O power supplied from F\_VDDIO (VDDIO9) = 1.8V on SOM board by default.  
VDDIO9: the input voltage of I/O group 9 for PF port (RGMII1).



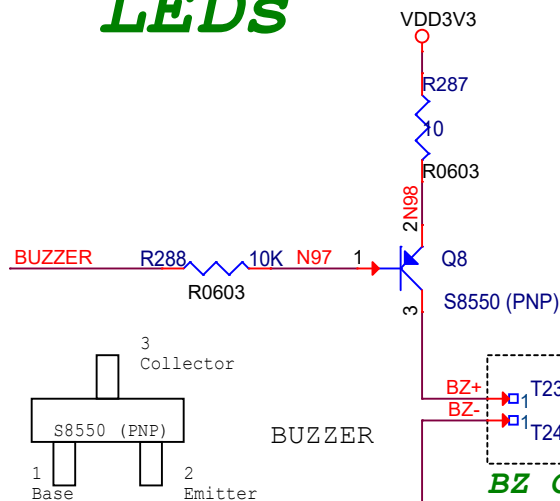
FAN\_EN = High, FAN ON  
FAN\_EN = Low, FAN OFF



## FAN

V2.1

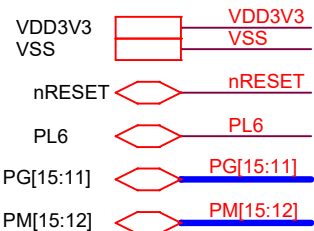
## LEDs



## Buzzer

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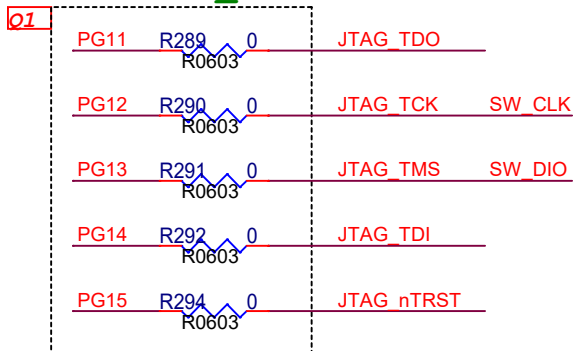
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Key Buttons, LEDs, Buzzer and Fan		
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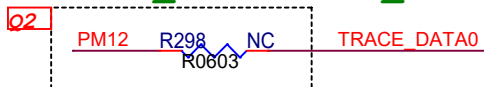
J7 HEADER 2x1 (2.54mm, male)

nRESET SWJ nRESET

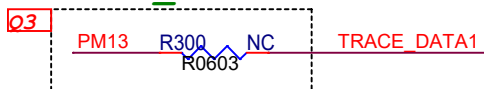
SWJ (EBI\_AD0/1/2/3/4)



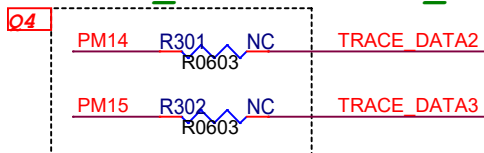
TRACE\_DATA0 (LCM\_RST)



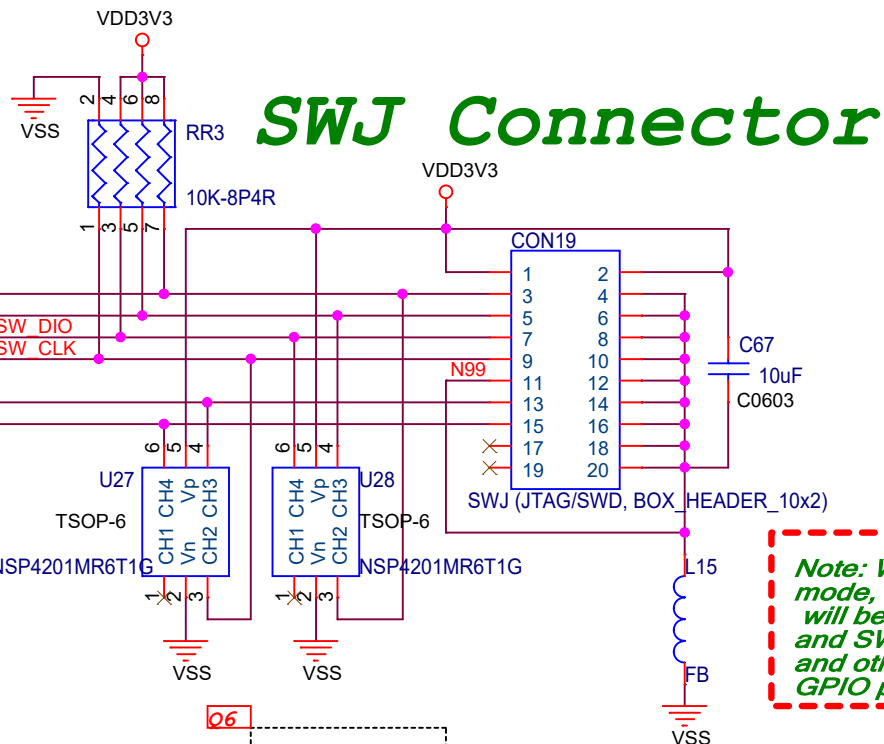
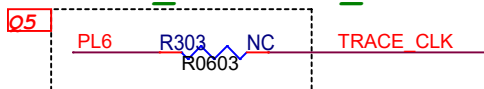
TRACE\_DATA1 (Buzzer)



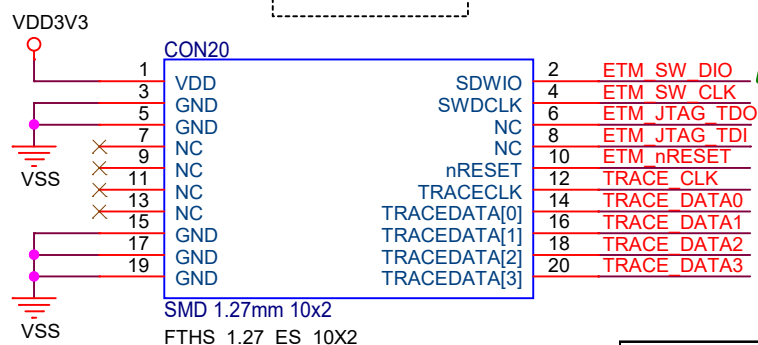
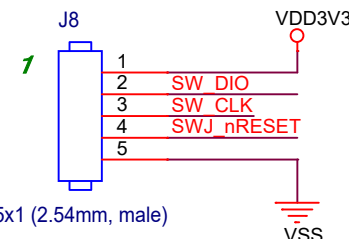
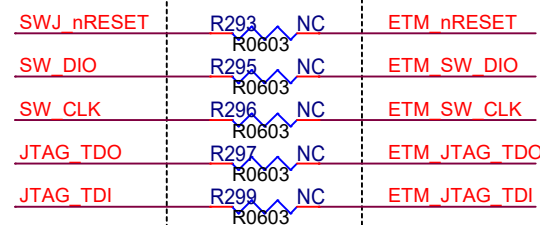
TRACE\_DATA2/3 (I2C\_3)



TRACE\_CLK (EBI\_AD5)



Note: When the chip enters debug mode, these GPIO PG11 ~ PG15 pins will be automatically forced to JTAG and SWD functionality by hardware, and other pinout functions of these GPIO pins will be disabled.



# SWD Connector

# ETM Connector

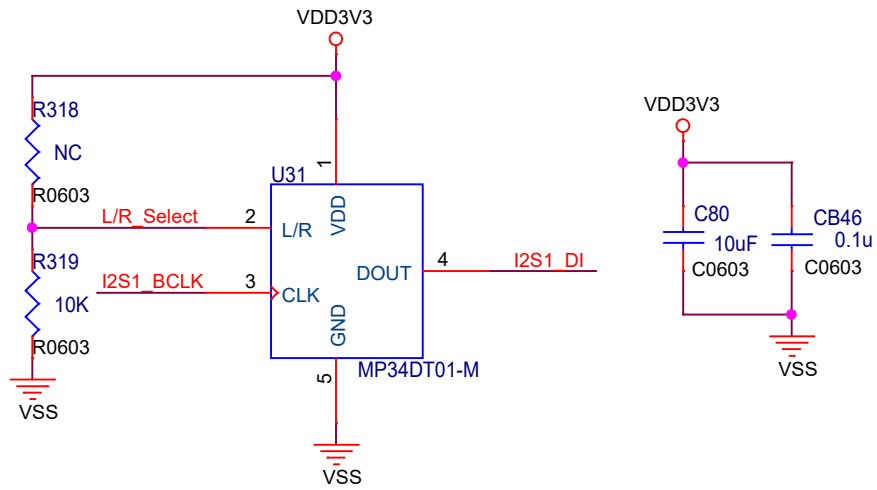
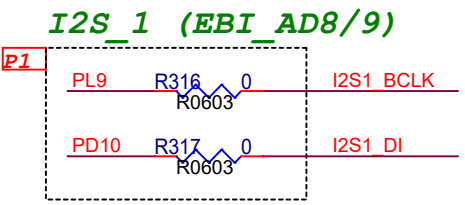
nuvoTon Technology Corp.

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NuMaker_MA35D1_Base		
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VDD3V3  
VSS



PL9  
PD10

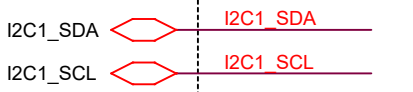


# MEMS Digital MIC

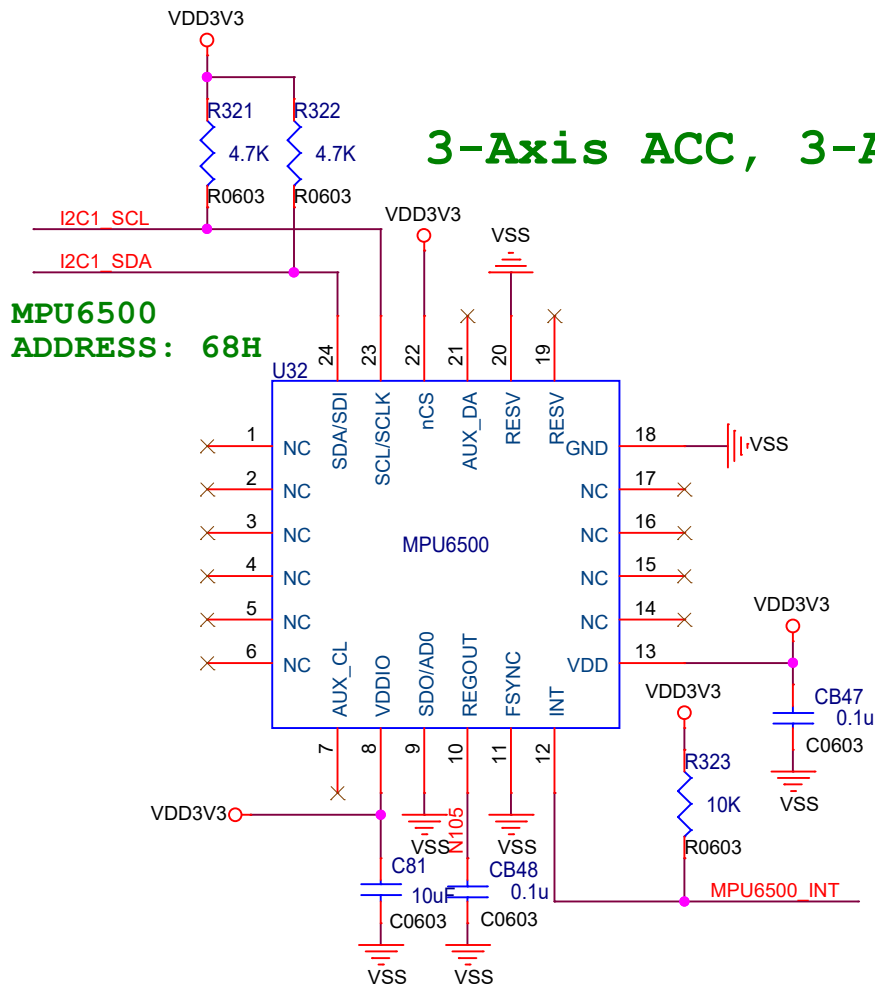
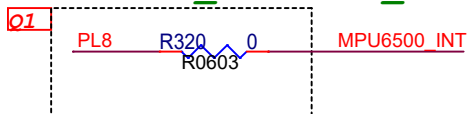
nuvoTon Technology Corp.			
Title NuMaker_MA35D1_Base			
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I2C1 SDA/SCL (LCM I2C1 SDA/SCL)



**MPU6500 INT (EBI AD7)**



## 3-Axis ACC, 3-Axis Gyro

# MEMS G-Sensor

**nuvoTon Technology Corp.**

Title

## NuMaker\_MA35D1\_Base

Size  
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Document Number

## MEMS G-Sensor (MPU6500)

Rev	V2.1
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