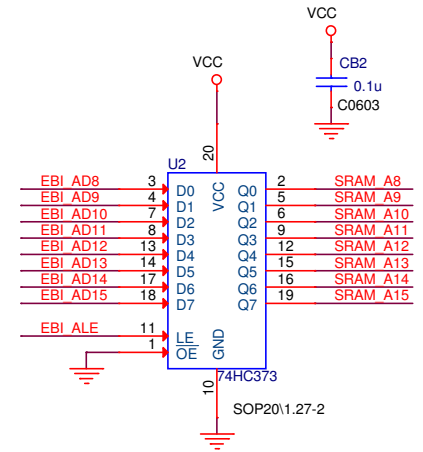



## SRAM Off-page

P13	P13	PA.11	SRAM A18
SRAM A17		PA 10	SRAM A17
SRAM A16		PA 9	SRAM A16
P126	P126	PG.7	EBI nWRL
P127	P127	PG.8	EBI nWRH
EBI_ALE		PA 8	EBI_ALE
EBI_nCS1		PD 11	EBI_nCS1
P111	P111	PE.5	EBI_nRD
P112	P112	PE.4	EBI_nWR
P122	P122	PH.11	EBI AD15
P121	P121	PH.10	EBI AD14
P120	P120	PH.9	EBI AD13
P119	P119	PH.8	EBI AD12
P118	P118	PE.0	EBI AD11
P117	P117	PE.1	EBI AD10
P71	P71	PE.15	EBI AD9
P70	P70	PE.14	EBI AD8
P75	P75	PD.9	EBI AD7
P76	P76	PD.8	EBI AD6
P90	P90	PG.14	EBI AD5
P89	P89	PG.13	EBI AD4
P88	P88	PG.12	EBI AD3
P87	P87	PG.11	EBI AD2
P86	P86	PG.10	EBI AD1
P85	P85	PG.9	EBI AD0
GND		GND	
VCC		VCC	

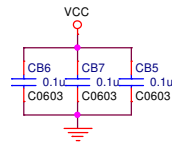
NOTE:  
SRAM address bus A16/A17/A18 are GPIO-controlled by MCU firmware. Not EBI hardware interface directly to drive.



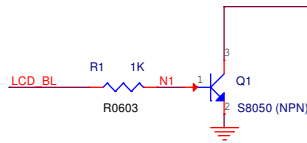
				
Title				
NuMaker M487 Advance (LV2)				
Size A	Document Number			Rev
	SRAM			V4.1
Date:	Thursday, September 19, 2019	Sheet	1 of 8	

## TFT LCD Off-page

P111	P111	PE.5	EBI nRD
P112	P112	PE.4	EBI nWR
P123	P123	PD.14	EBI nCS0
P122	P122	PH.11	EBI AD15
P121	P121	PH.10	EBI AD14
P120	P120	PH.9	EBI AD13
P119	P119	PH.8	EBI AD12
P118	P118	PE.0	EBI AD11
P117	P117	PE.1	EBI AD10
P71	P71	PE.15	EBI AD9
P70	P70	PE.14	EBI AD8
P75	P75	PD.9	EBI AD7
P76	P76	PD.8	EBI AD6
P90	P90	PG.14	EBI AD5
P89	P89	PG.13	EBI AD4
P88	P88	PG.12	EBI AD3
P87	P87	PG.11	EBI AD2
P86	P86	PG.10	EBI AD1
P85	P85	PG.9	EBI AD0
P40	P40	PH.3	LCD RS
P144	P144	PB.6	LCD RESET
P143	P143	PB.7	LCD BL
P141	P141	PB.9	LCD XR
P142	P142	PB.8	LCD YU
P41	P41	PH.4	LCD XL
P42	P42	PH.5	LCD YD
		GND	VCC



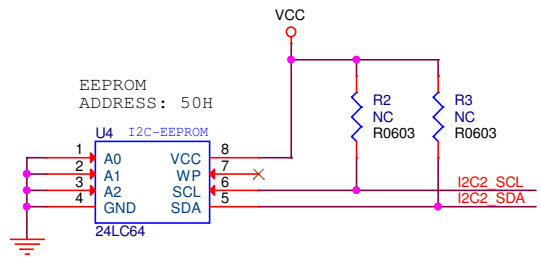
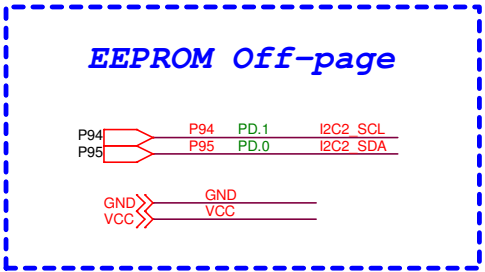
GND	1
VCC	2
VCC	3
EBI nCS0	4
LCD RS	5
EBI nWR	6
EBI nRD	7
LCD RESET	8
EBI AD0	9
EBI AD1	10
EBI AD2	11
EBI AD3	12
EBI AD4	13
EBI AD5	14
EBI AD6	15
EBI AD7	16
EBI AD8	17
EBI AD9	18
EBI AD10	19
EBI AD11	20
EBI AD12	21
EBI AD13	22
EBI AD14	23
EBI AD15	24
GND	25
LCD YD	26
LCD XR	27
LCD YU	28
LCD XL	29
LEDK	30
	31
	32
	33
VCC	34
VCC	35
GND	36
GND	37



PFC CONN 37-pin (1.0 pitch)

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Title		
NuMaker M487 Advance (LV2)		
Size	Document Number	Rev
A4	3.2" TFT LCD Module	V4.1
Date:	Thursday, September 19, 2019	Sheet 2 of 8



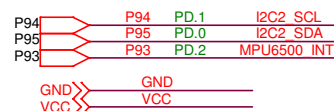
# I2C EEPROM

**nuvoTon**

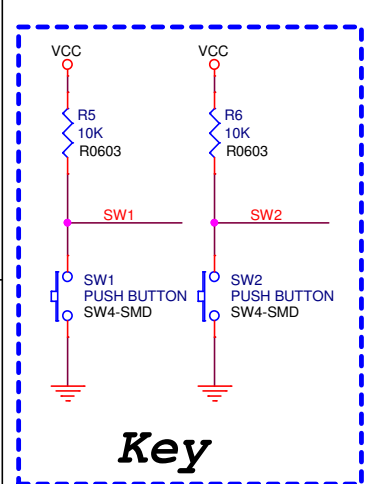
Title		
NuMaker M487 Advance (LV2)		
Size	Document Number	Rev
A	I2C EEPROM	V4.1
Date: Thursday, September 19, 2019		Sheet 3 of 8

[illegible]

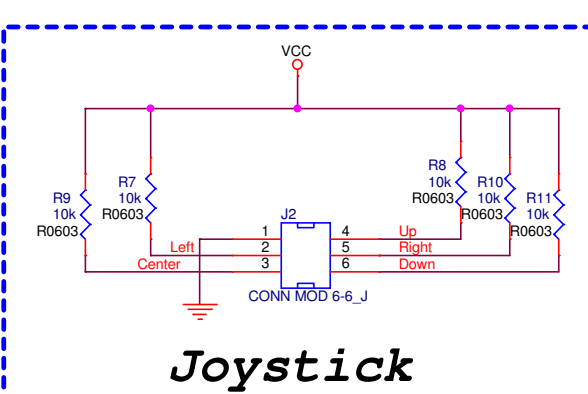
**MEMS Off-page**



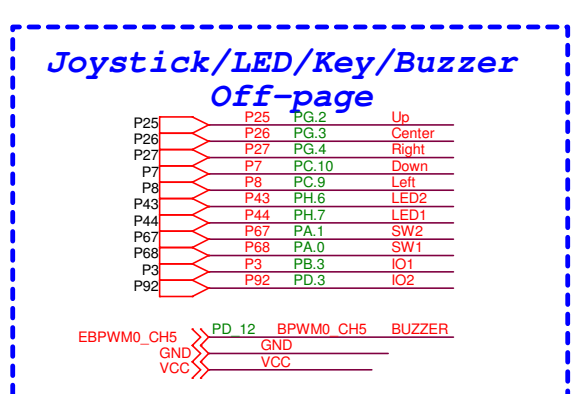
Title			
NuMaker M487 Advance (LV2)			
Size A	Document Number		Rev
	MEMS (MPU6500)		V4.1
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**Key**

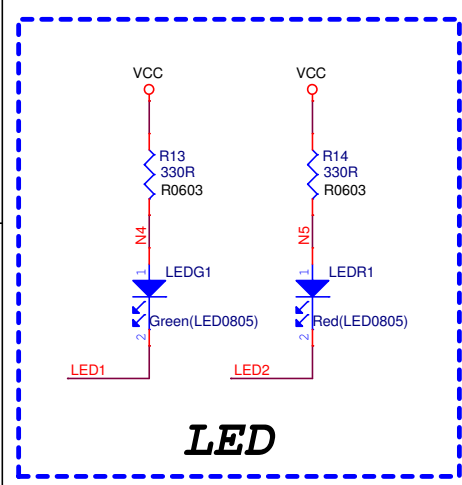
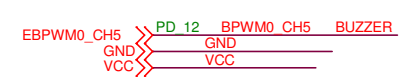


**Joystick**

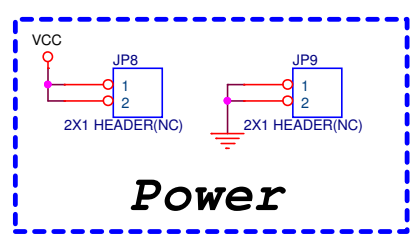


**Joystick/LED/Key/Buzzer**  
**Off-page**

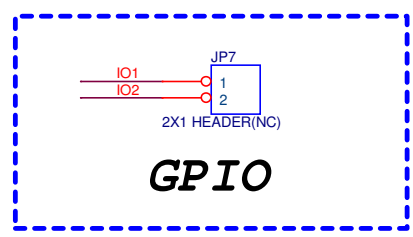
P25	P25	PG.2	Up
P26	P26	PG.3	Center
P27	P27	PG.4	Right
P7	P7	PC.10	Down
P8	P8	PC.9	Left
P43	P43	PH.6	LED2
P44	P44	PH.7	LED1
P67	P67	PA.1	SW2
P68	P68	PA.0	SW1
P3	P3	PB.3	IO1
P92	P92	PD.3	IO2



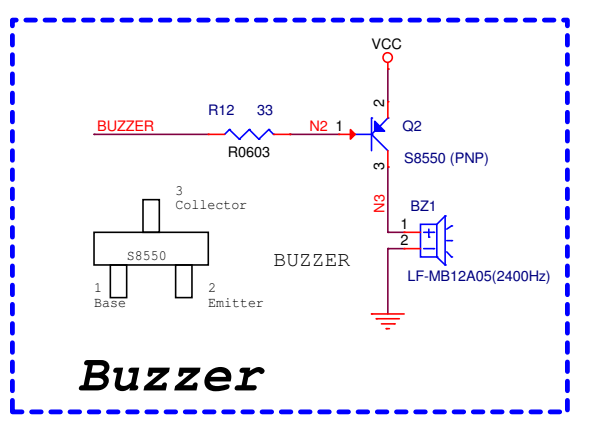
**LED**



**Power**

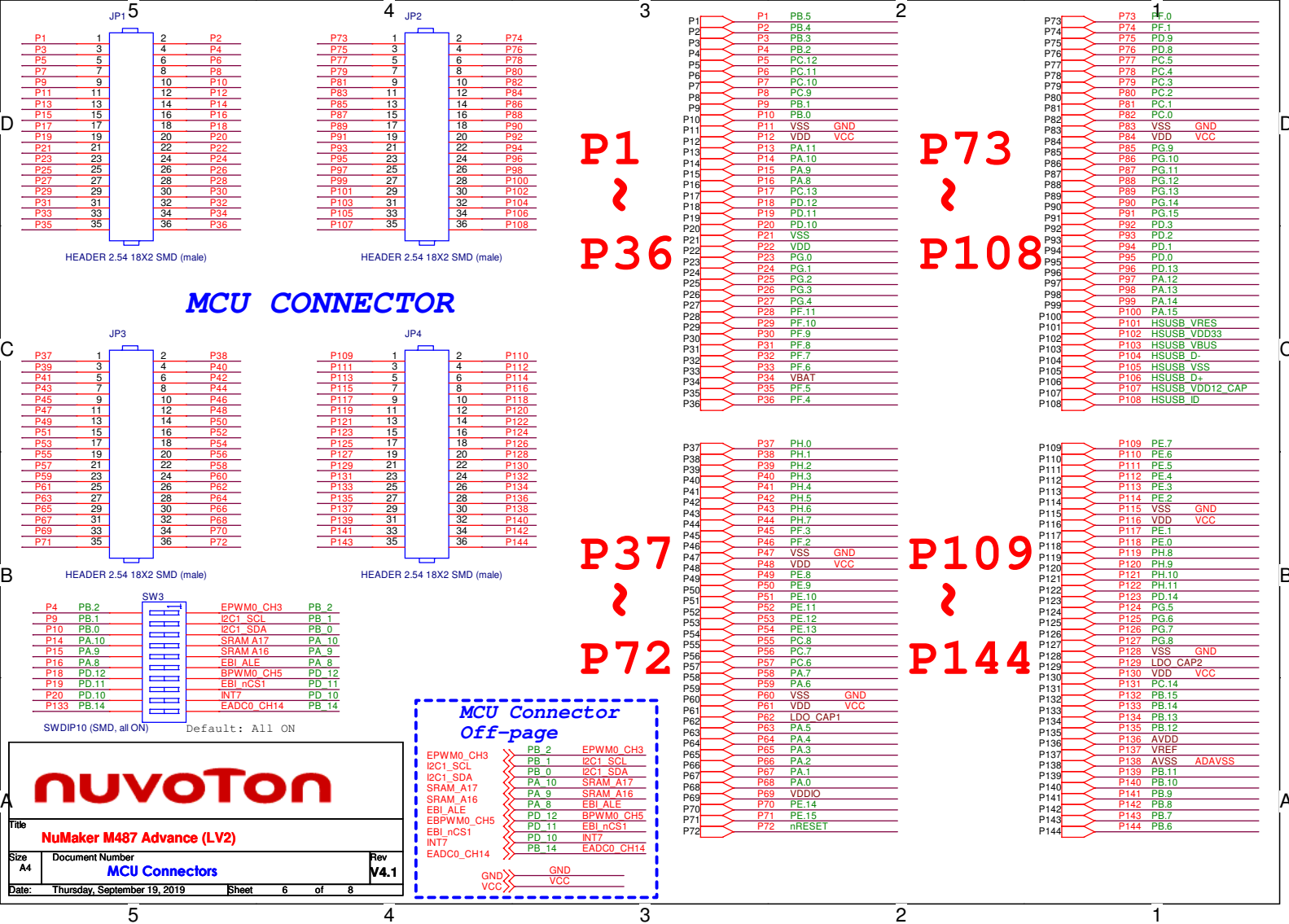


**GPIO**



**Buzzer**

Title		
NuMaker M487 Advance (LV2)		
Size A	Document Number	Rev
	Joystick, LED, Keys and Buzzer	V4.1
Date:	Thursday, September 19, 2019	Sheet 5 of 8



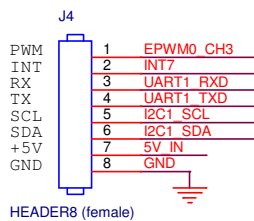
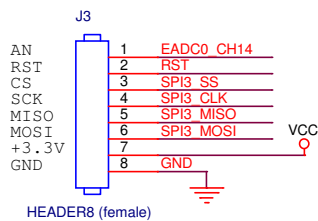
## Mikro Bus Off-page

EPWM0\_CH3  
I2C1\_SCL  
I2C1\_SDA  
INT7  
EADC0\_CH14

PB\_2 EPWM0\_CH3  
PB\_1 I2C1\_SCL  
PB\_0 I2C1\_SDA  
PD\_10 INT7  
PB\_14 EADC0\_CH14

P23 P23 PG.0 UART1\_TXD  
P24 P24 PG.1 UART1\_RXD  
P124 P124 PG.5 SPI3\_SS  
P125 P125 PG.6 SPI3\_CLK  
P5 P5 PC.12 SPI3\_MISO  
P6 P6 PC.11 SPI3\_MOSI  
P72 P72 nRESET RST  
P97 P97 PA.12 5V\_IN  
P103 P103 HSUSB\_VBUS 5V\_IN

GND GND  
VCC VCC



# Mikro Bus

## nuvoTon

Title

**NuMaker M487 Advance (LV2)**

Size

Document Number

**Mikro Bus**

Rev

**V4.1**

Date:

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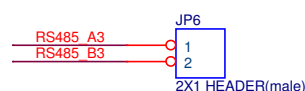
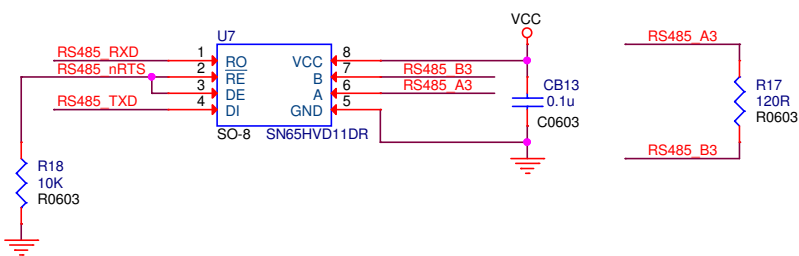
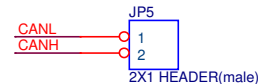
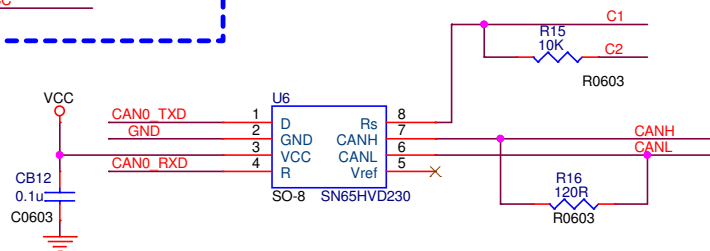
of

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## CAN and RS485 Off-page

P63	P63	PA.5	CAN0_TXD
P64	P64	PA.4	CAN0_RXD
P65	P65	PA.3	C2
P66	P66	PA.2	C1
P37	P37	PH.0	RS485_TXD
P38	P38	PH.1	RS485_RXD
P39	P39	PH.2	RS485_nRTS

GND VCC  
VCC VCC



# CAN and RS485

# nuvoTon

Title		
NuMaker M487 Advance (LV2)		
Size	Document Number	Rev
A	CAN and RS485	V4.1
Date:	Thursday, September 19, 2019	Sheet 8 of 8