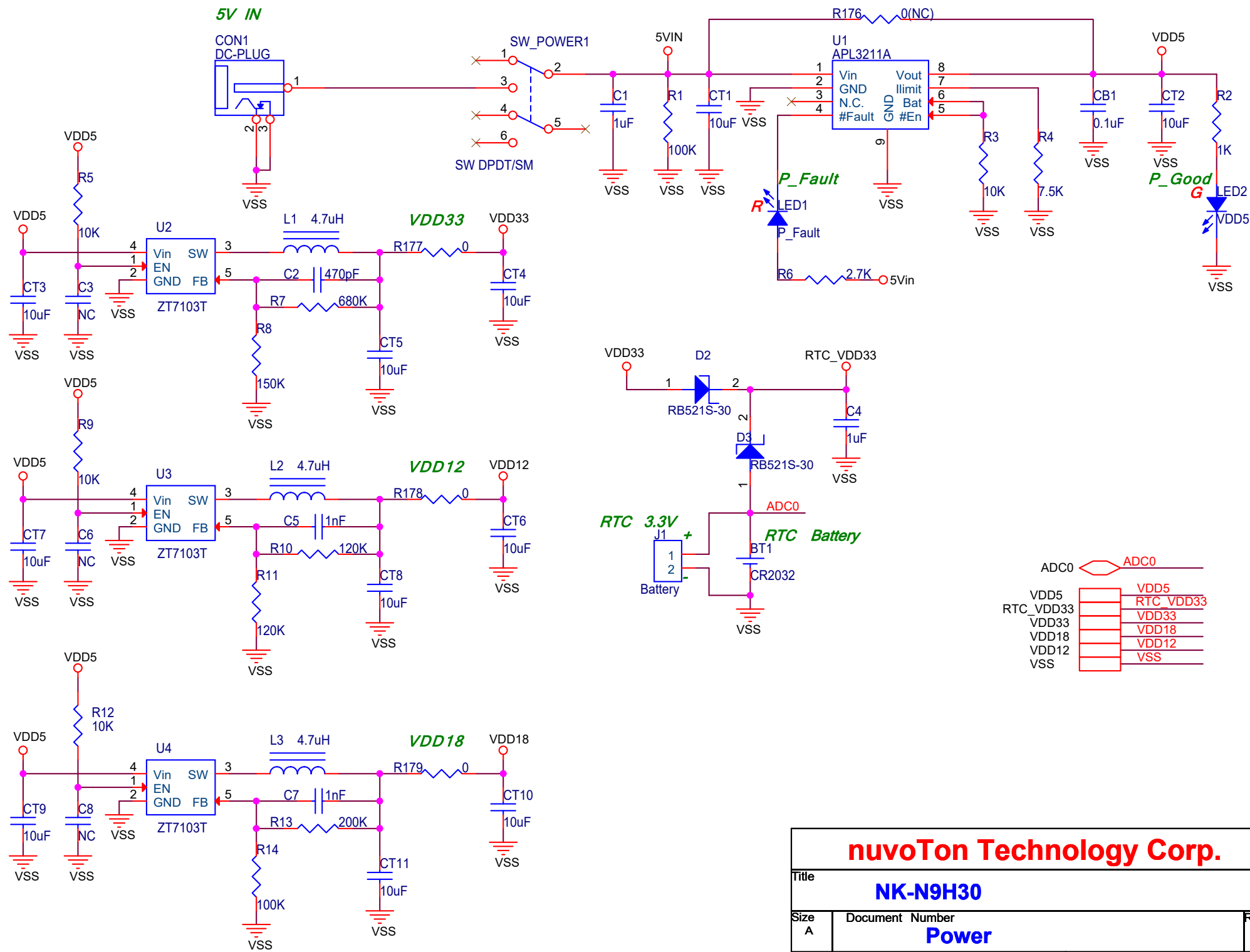


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A	System Block	2.1
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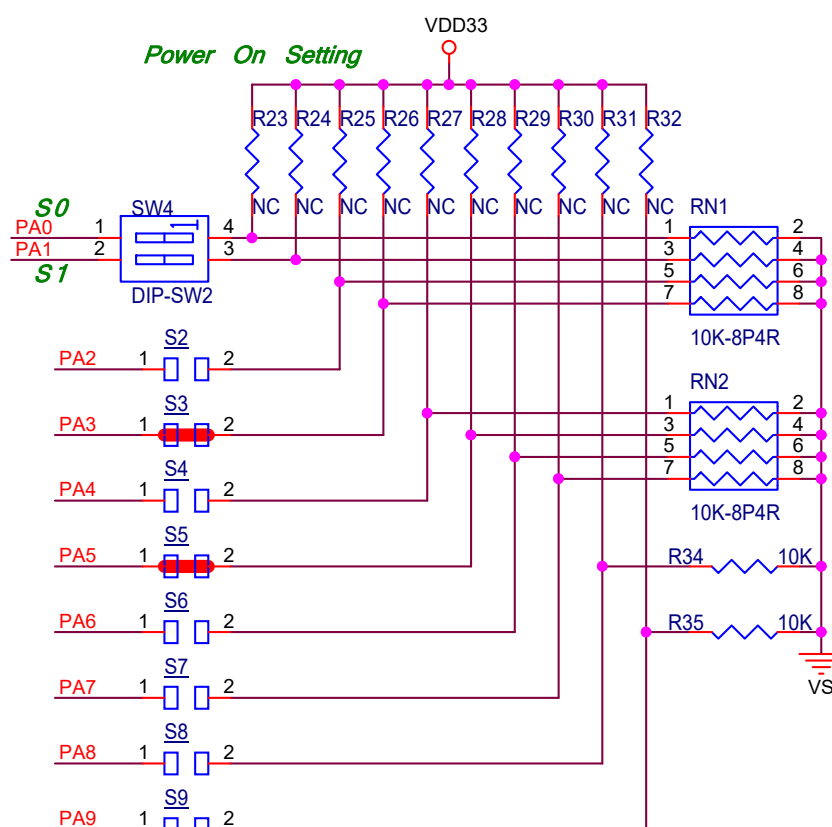




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A	<b>Power</b>	2.1
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S0(A1)	S1(A0)	Boot
ON	ON	USB
ON	OFF	eMMC
OFF	ON	NAND
OFF	OFF	SPI

S2(A2)	Clock
O	12MHz
X	UPLL

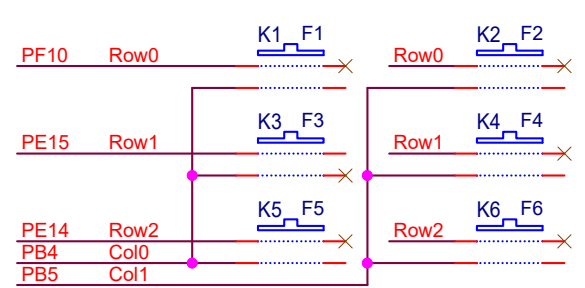
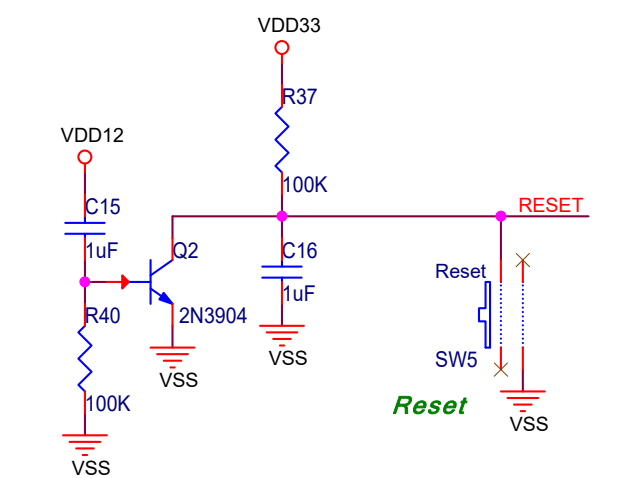
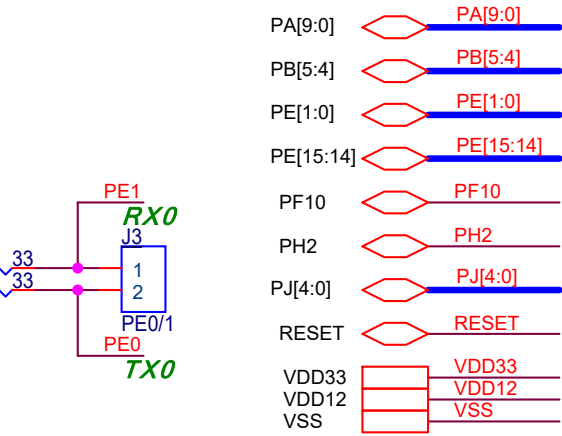
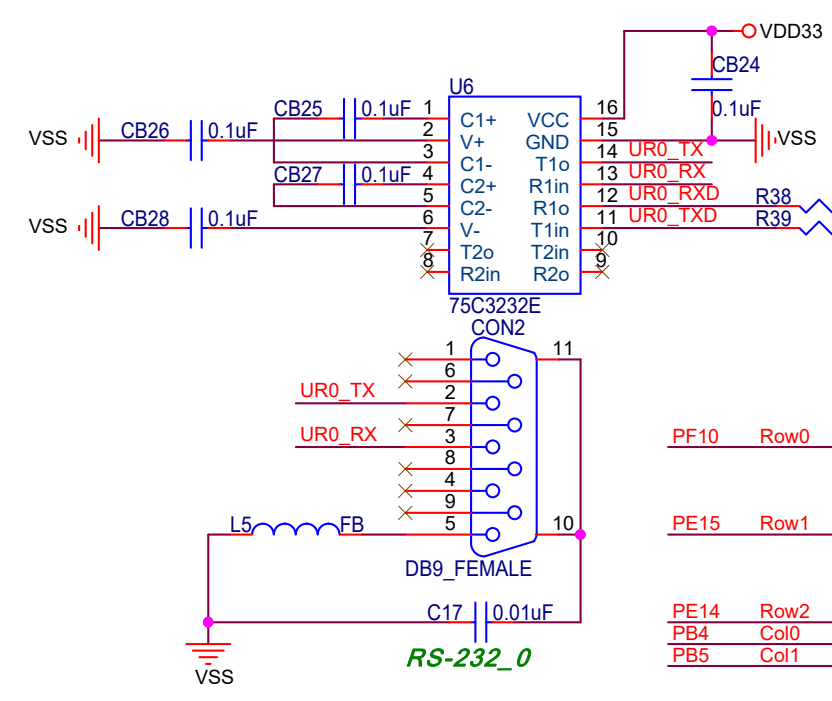
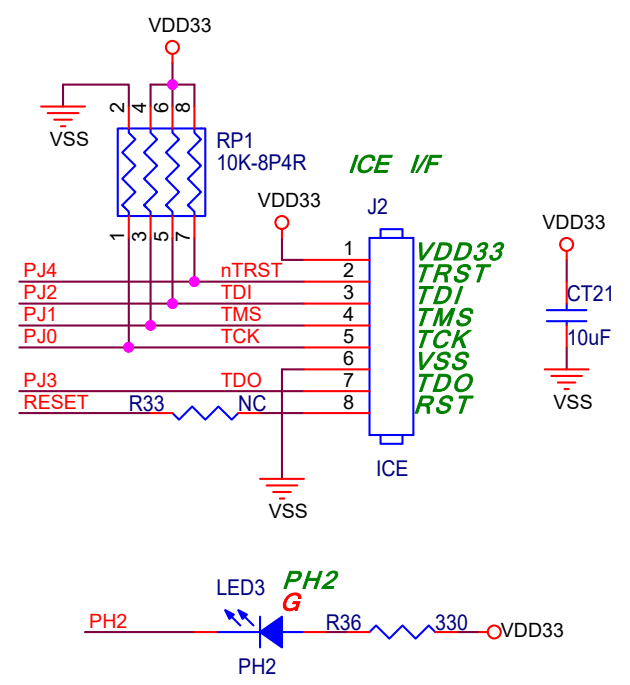
S3(A3)	Watch Dog
O	OFF
X	ON

S4(A4)	PJ[4:0]
O	GPIO
X	JTAG

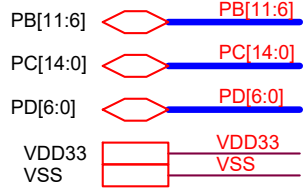
S5(A5)	UART0 Message
O	ON
X	OFF

S7(A7)	S6(A6)	NAND Page Size
O	O	2KB
O	X	4KB
X	O	8KB
X	X	None

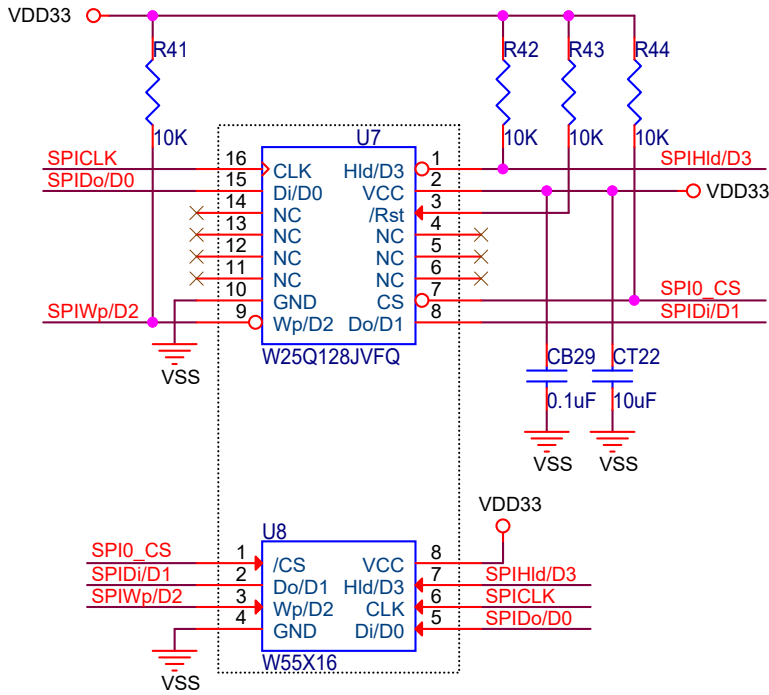
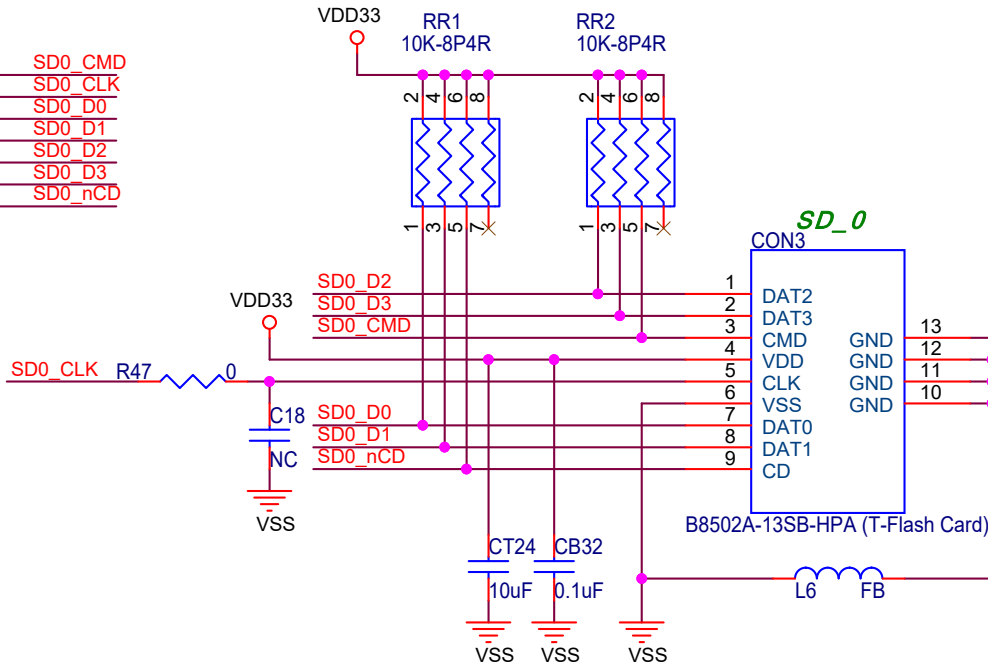
S9(A9)	S8(A8)	NAND ECC
O	O	T12
O	X	T15
X	O	T24
X	X	None



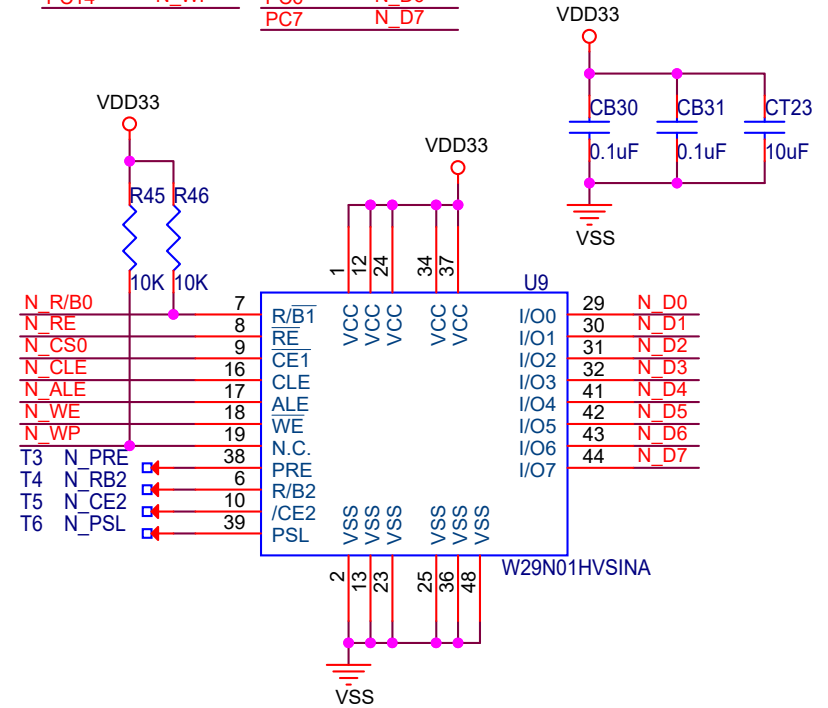
PB6	SPI0_CS
PB7	SPICLK
PB8	SPIDo/D0
PB9	SPIDi/D1
PB10	SPIWp/D2
PB11	SPIHid/D3



PD0	SD0_CMD
PD1	SD0_CLK
PD2	SD0_D0
PD3	SD0_D1
PD4	SD0_D2
PD5	SD0_D3
PD6	SD0_nCD

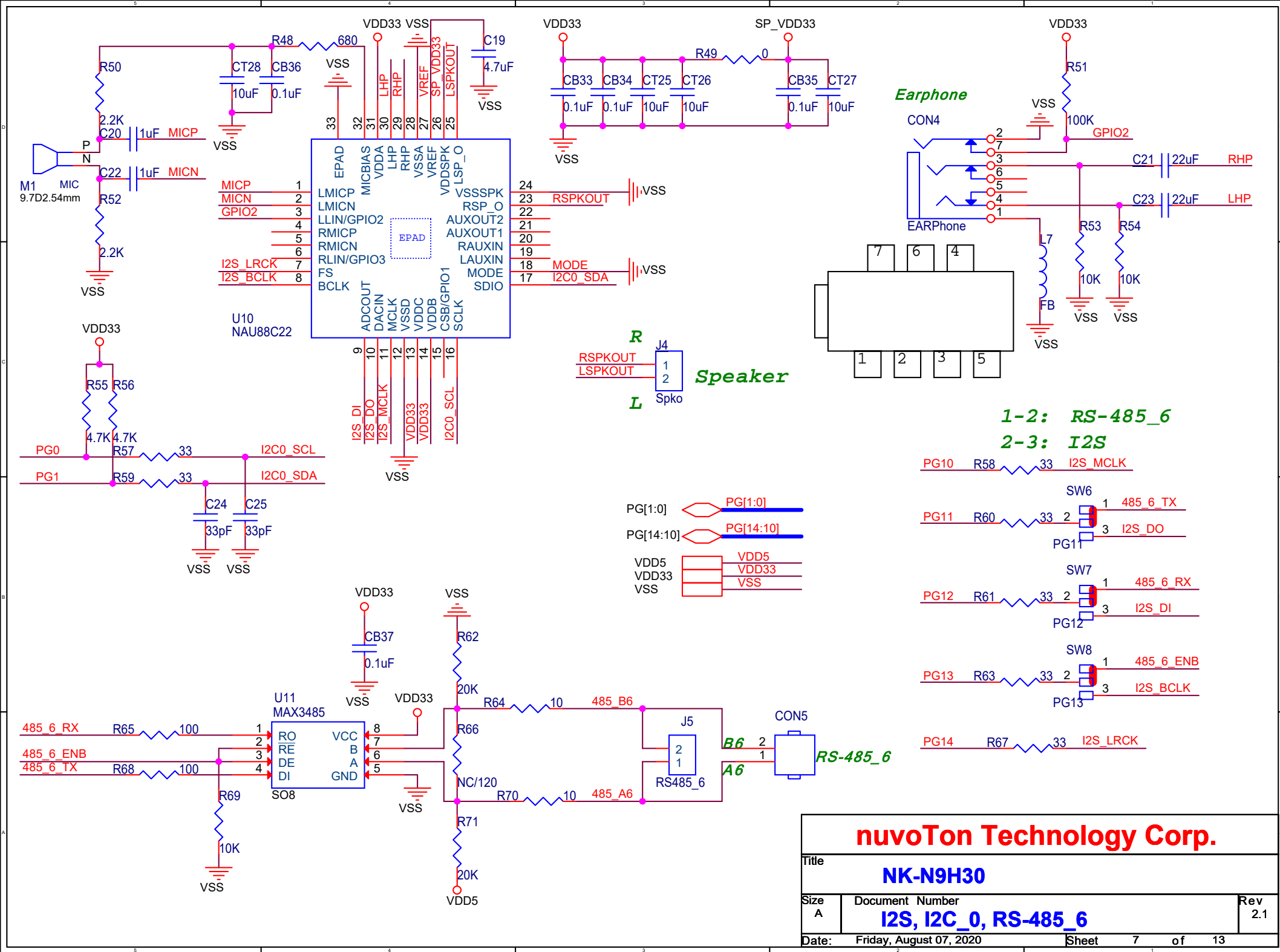


PC8	N_CS0	PC0	N_D0
PC9	N_ALE	PC1	N_D1
PC10	N_CLE	PC2	N_D2
PC11	N_WE	PC3	N_D3
PC12	N_RE	PC4	N_D4
PC13	N_R/B0	PC5	N_D5
PC14	N_WP	PC6	N_D6
		PC7	N_D7

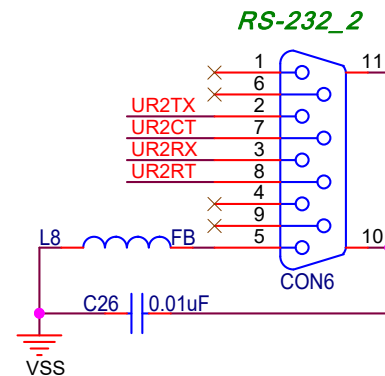
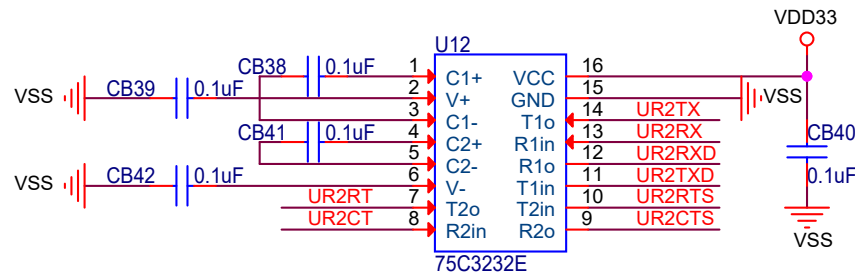
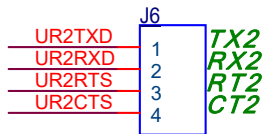


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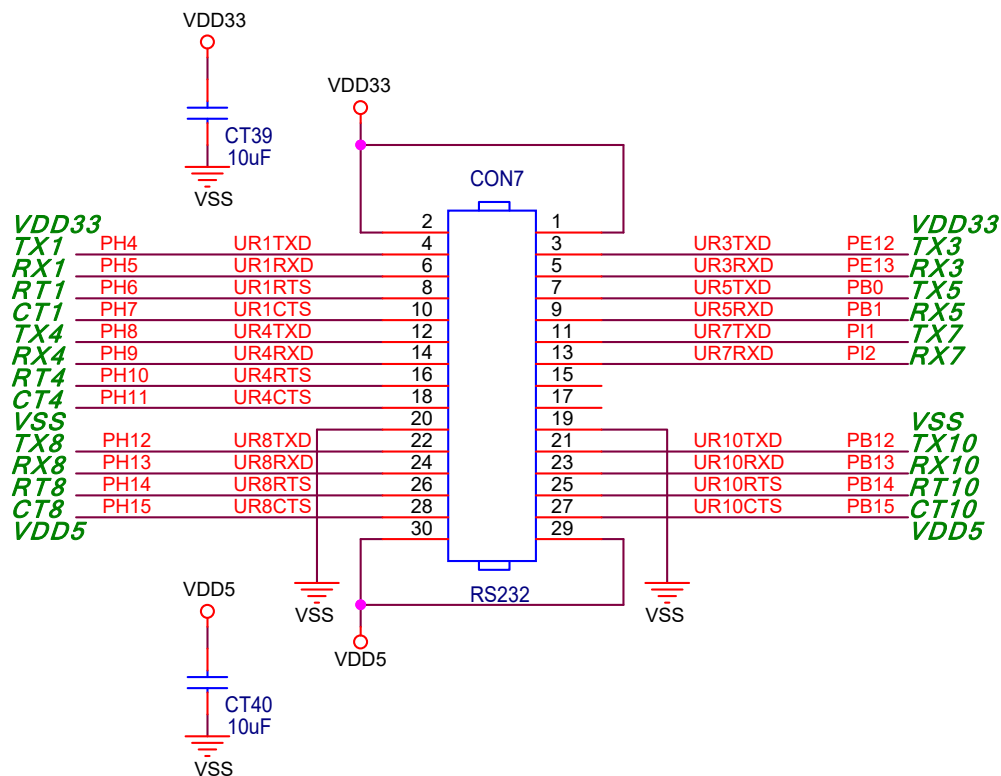


PF11 UR2TXD  
PF12 UR2RXD  
PF13 UR2RTS  
PF14 UR2CTS



PB[1:0] PB[1:0]  
PB[15:12] PB[15:12]  
PE[13:12] PE[13:12]  
PF[14:11] PF[14:11]  
PH[15:4] PH[15:4]  
PI[2:1] PI[2:1]

VDD5  
VDD33  
VSS



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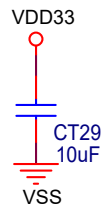
PA0	R72	33	LCD_D0	B0
PA1	R73	33	LCD_D1	B1
PA2	R74	33	LCD_D2	B2
PA3	R75	33	LCD_D3	B3
PA4	R76	33	LCD_D4	B4
PA5	R77	33	LCD_D5	B5
PA6	R78	33	LCD_D6	B6
PA7	R79	33	LCD_D7	B7
PA8	R80	33	LCD_D8	G0
PA9	R81	33	LCD_D9	G1
PA10	R82	33	LCD_D10	G2
PA11	R83	33	LCD_D11	G3
PA12	R89	33	LCD_D12	G4
PA13	R84	33	LCD_D13	G5
PA14	R85	33	LCD_D14	G6
PA15	R86	33	LCD_D15	G7
PD8	R87	33	LCD_D16	R0
PD9	R90	33	LCD_D17	R1
PD10	R88	33	LCD_D18	R2
PD11	R91	33	LCD_D19	R3
PD12	R92	33	LCD_D20	R4
PD13	R93	33	LCD_D21	R5
PD14	R94	33	LCD_D22	R6
PD15	R95	33	LCD_D23	R7

PG6	R96	33	LCD_CLK	
PG7	R97	33	LCD_HSYNC	
PG8	R98	33	LCD_VSYNC	
PG9	R99	33	LCD_DEN	

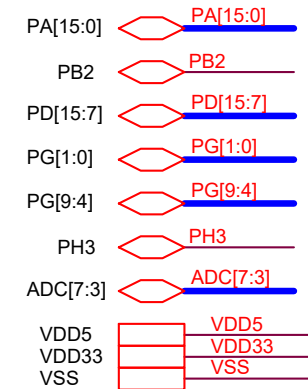
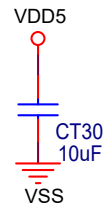
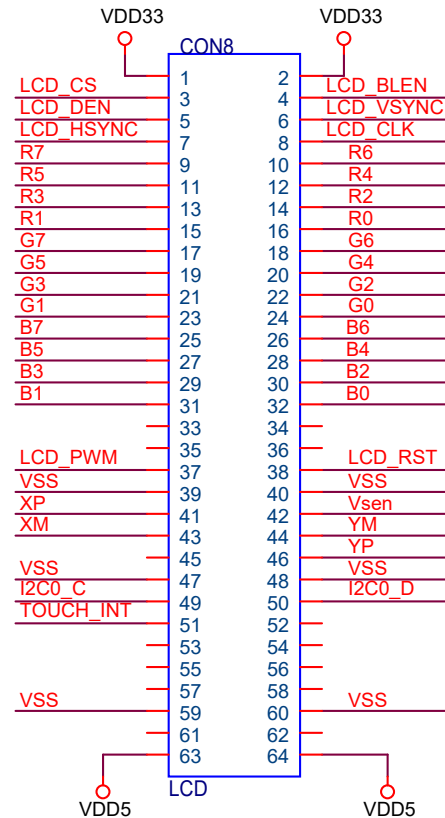
PB2	R100	33	LCD_PWM	
PD7	R101	33	LCD_CS	
PH3	R102	33	LCD_BLEN	
PG4	R103	33	LCD_RST	

ADC3	R104	0	Vsen	
ADC4	R105	0	YM	
ADC5	R106	0	YP	
ADC6	R107	0	XM	
ADC7	R108	0	XP	
PG5	R109	33	TOUCH_INT	

PG0	R110	33	I2C0_C	
PG1	R111	33	I2C0_D	

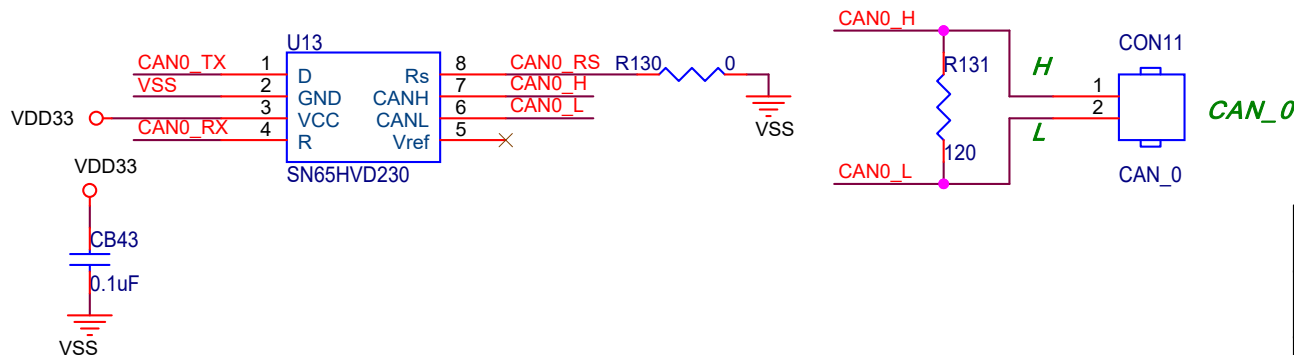
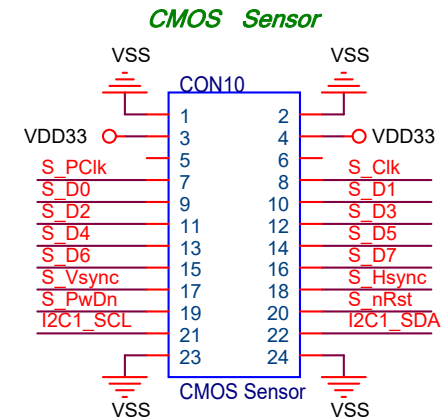
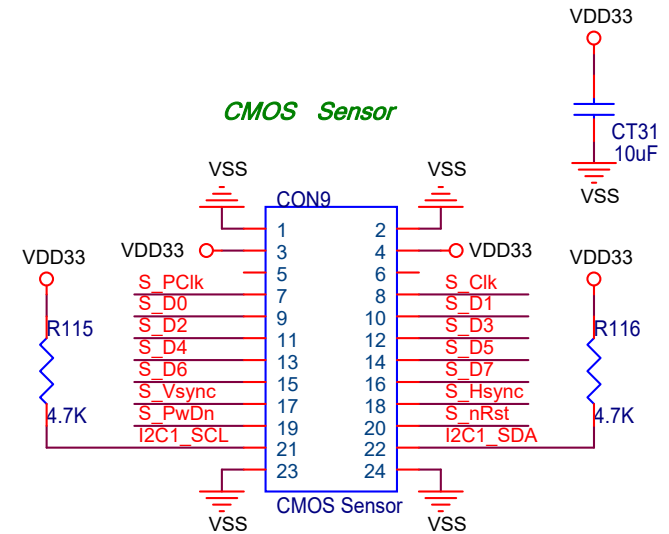
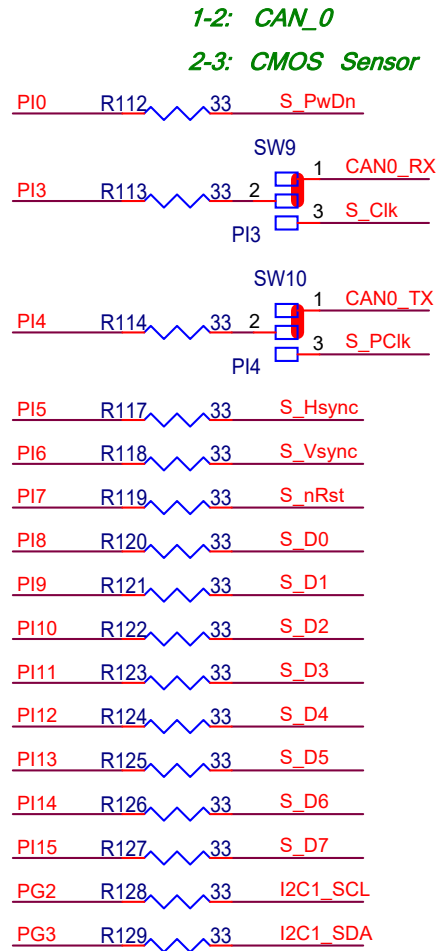
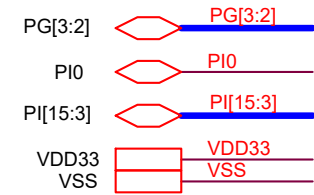


## 24Bit LCD Interface RGB 888



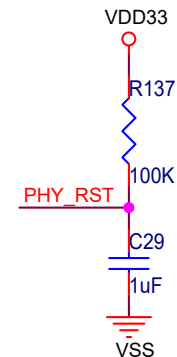
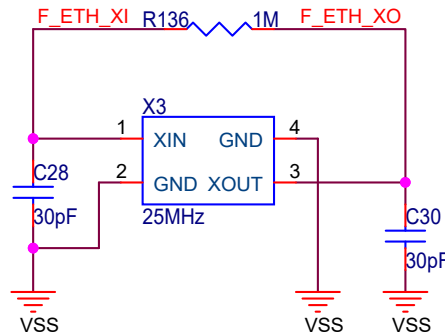
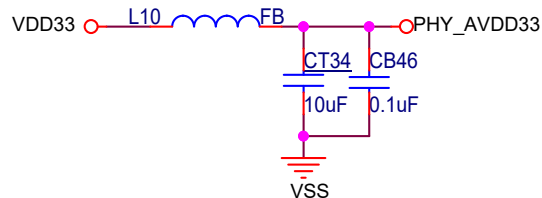
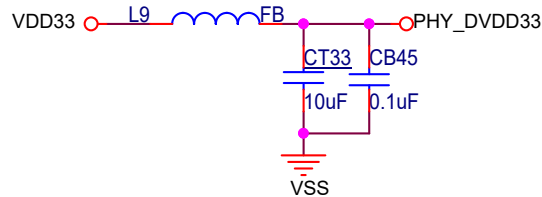
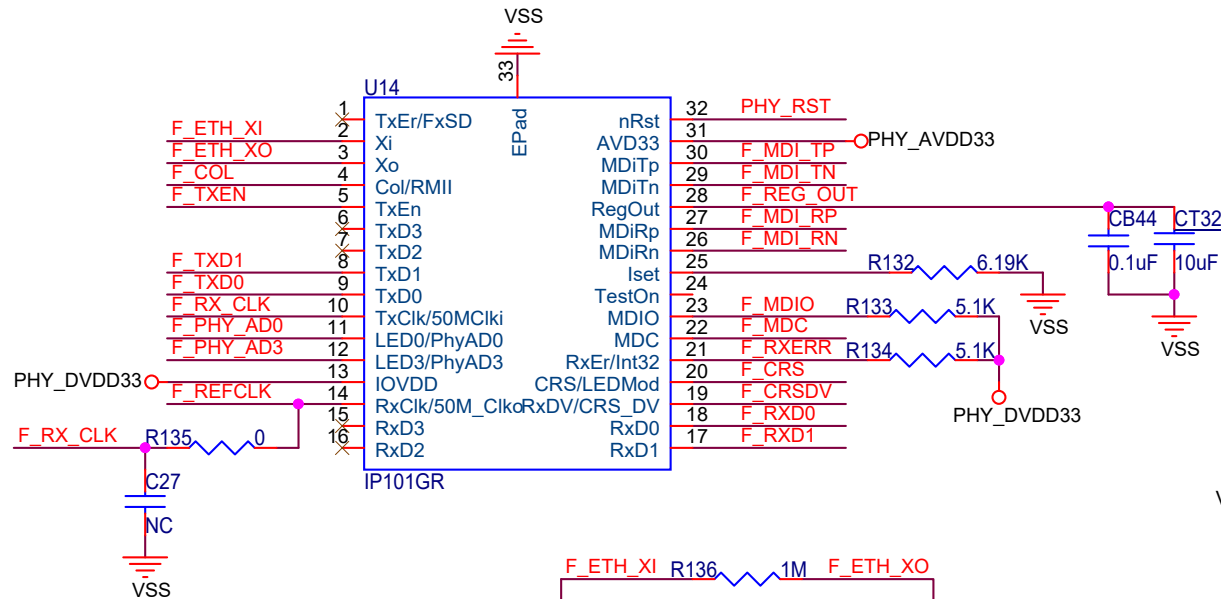
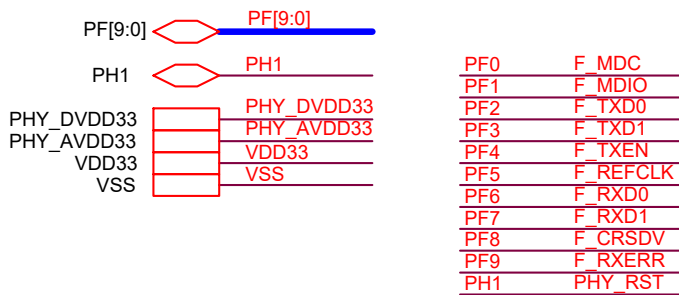
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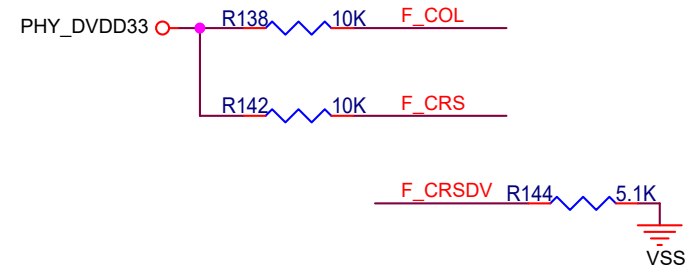
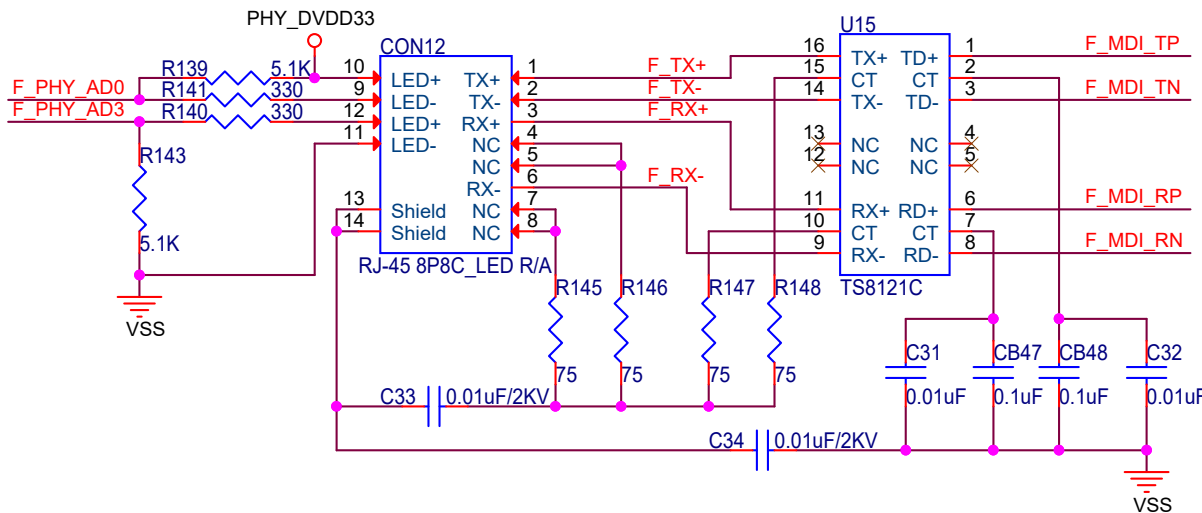


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A	CMOS Sensor, I2C_1, CAN_0	2.1
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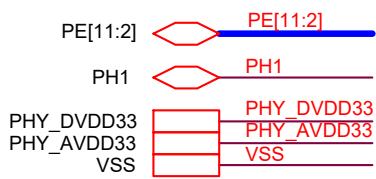
### Ethernet0\_PF



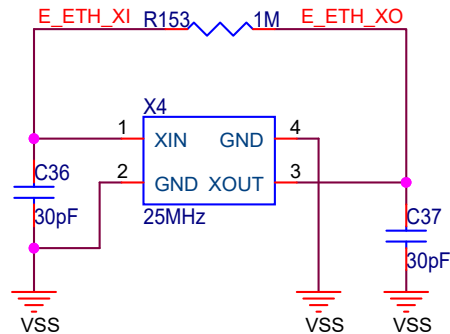
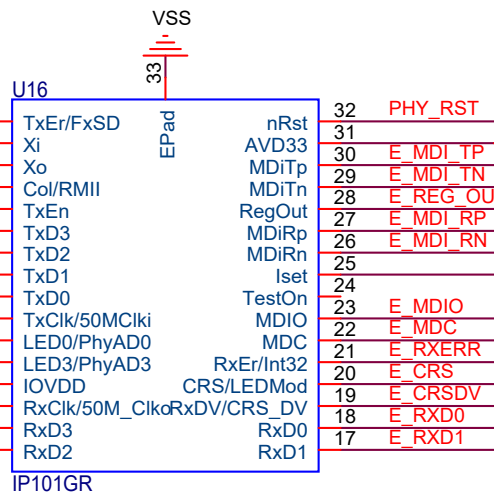
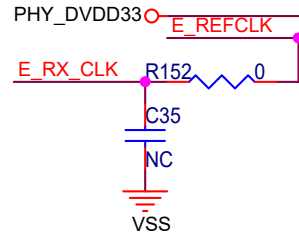
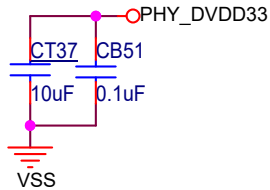
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PE2	E_MDC
PE3	E_MDIO
PE4	E_TXD0
PE5	E_TXD1
PE6	E_TXEN
PE7	E_REFCLK
PE8	E_RXD0
PE9	E_RXD1
PE10	E_CRSDV
PE11	E_RXERR
PH1	PHY_RST



### Ethernet1\_PE

