

**ARM[®] Cortex[®]-M0
32-bit Microcontroller**

**NuMicro[®] Family
M0518 Series BSP
Revision History**

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Revision 3.00.004 (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Fixed CLK_SetHCLK() bug of CLK driver.
4. Fixed CLK_EnablePLL() wrong PLL default setting value of CLK driver

Revision 3.00.003 (Released 2017-10-05)

1. Fix clear Time-out flag method bug in I2C_ClearTimeoutFlag() of I²C driver.
2. Fix bug of PWM_ConfigOutputChannel() and PWM_ConfigCaptureChannel() for counter type setting error when u32ChannelNum is 1,3,5.
3. Fix bug for revised PWM_Stop() implementation
4. Fix bug for return value error of PWM_ConfigOutputChannel()
5. Add ADC_MeasureAVDD sample code
6. Add new function to control systick and select systick clock source, CLK_EnableSysTick() and CLK_DisableSysTick()
7. Add CLK_SysTickLongDelay() for long delay
8. Fix UART_SelectLINMode() clear enable bit setting bug
9. Fix PLL clock source selection bug in CLK_SetCoreClock()
10. Fix clear Receive Line Status interrupt flag bug in UART_ClearIntFlag().

Revision 3.00.002 (Released 2014-12-23)

1. Fix PWM driver bug for output low when duty is 100%
2. Fix BPWM driver bug for output low when duty is 100%
3. Fix CLK driver bug in CLK_SetCoreClock():
4. Fix CLK driver constant definitions error of (B)PWM0/1_MODULE clock source selection.
5. Fix GPIO_ENABLE_DOUT_MASK() and GPIO_DISABLE_DOUT_MASK() bug of GPIO driver.
6. Fix PWM driver bug of PWM_MASK_OUTPUT() to remove redundant parenthesis.
7. Fix BPWM driver bug of BPWM_MASK_OUTPUT() to remove redundant parenthesis
8. Fix UART driver clear flag bug in UART_ClearIntFlag()
9. Fix I2C driver module reset bug of I2C_Close().
10. Fix clear RS-485 address byte detection flag bug in UART_RS485_CLEAR_ADDR_FLAG() of UART driver.
11. Fix clear RS-485 address byte detection flag clear bug in RS485_HANDLE() of UART_RS485_Slave sample code.
12. Fix NVIC_EnableIRQ() to NVIC_DisableIRQ() after CHIP wake-up in I2C_Wakeup_Slave sample code
13. Fix UART RS485 RTS active level to high level active in RS485_9bitModeMaster() of UART RS485 Sample code.
14. Add PWM_EnableLoadMode() and PWM_DisableLoadMode() functions to PWM driver
15. Add PWM_SetBrakePinSource() function to PWM driver
16. Add CLK_GetPCLKFreq() function to CLK driver
17. Add new macro PWM_SET_DEADZONE_CLK_SRC() to PWM driver
18. Add nonblocking printf implementation and use predefine compiler option to enable/disable it.

Revision 3.00.002 (Released 2014-07-18)

1. Fixed constant definitions of Timer 2 and Timer 3 in clk.h
2. Fixed SYS_Init() GPIO initial bug of \SampleCode\StdDriver\ACMP.

Revision 3.00.001 (Released 2014-06-12)

1. First Release

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